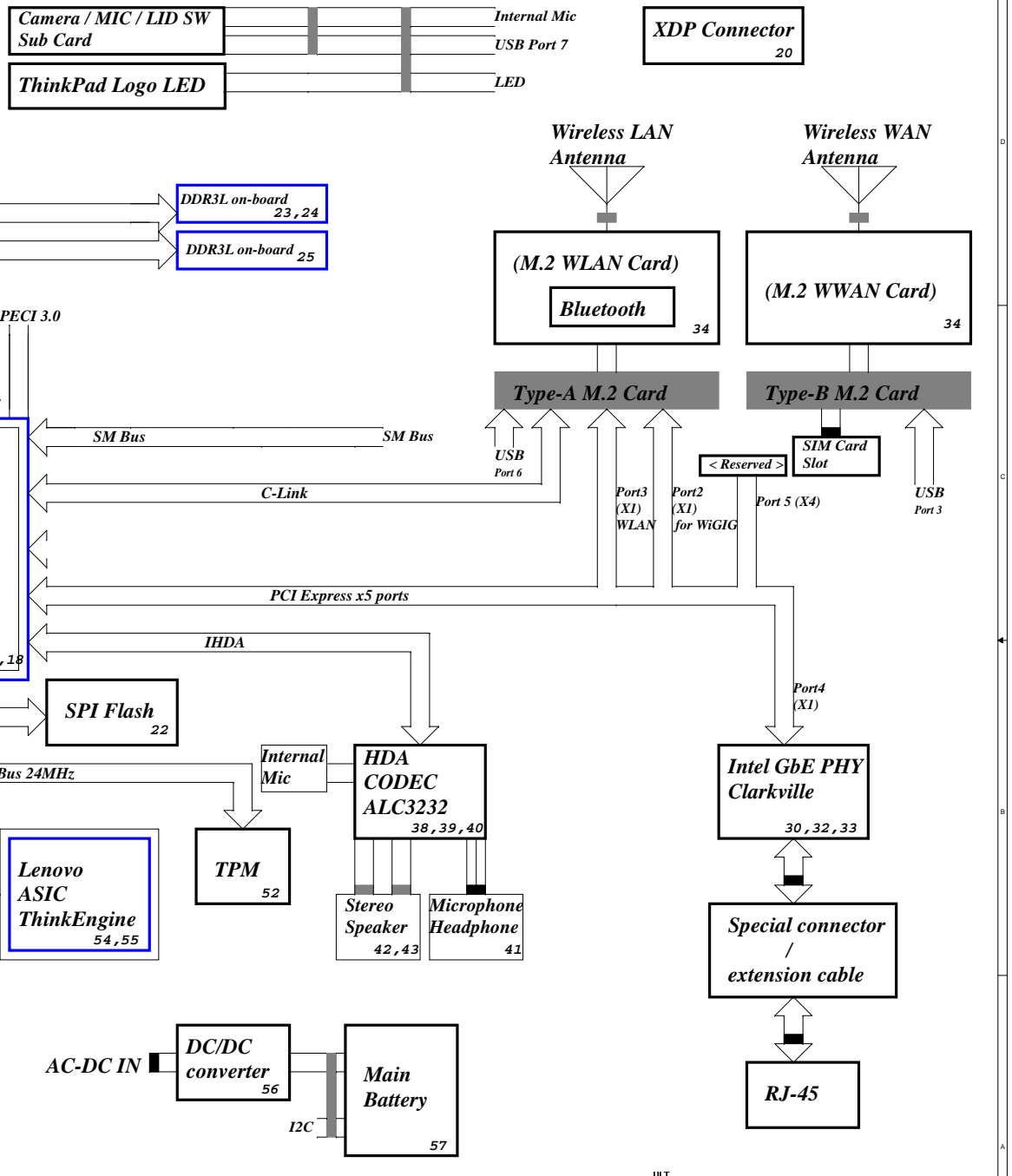


Mystique-1 Shark Bay Block Diagram

Project Code: 91.4LY01.001

PCB(Raw Card): 12298-2



■ External Connector/Socket
■ Internal Connector/Socket
⊗ Internal Switch

RESISTOR

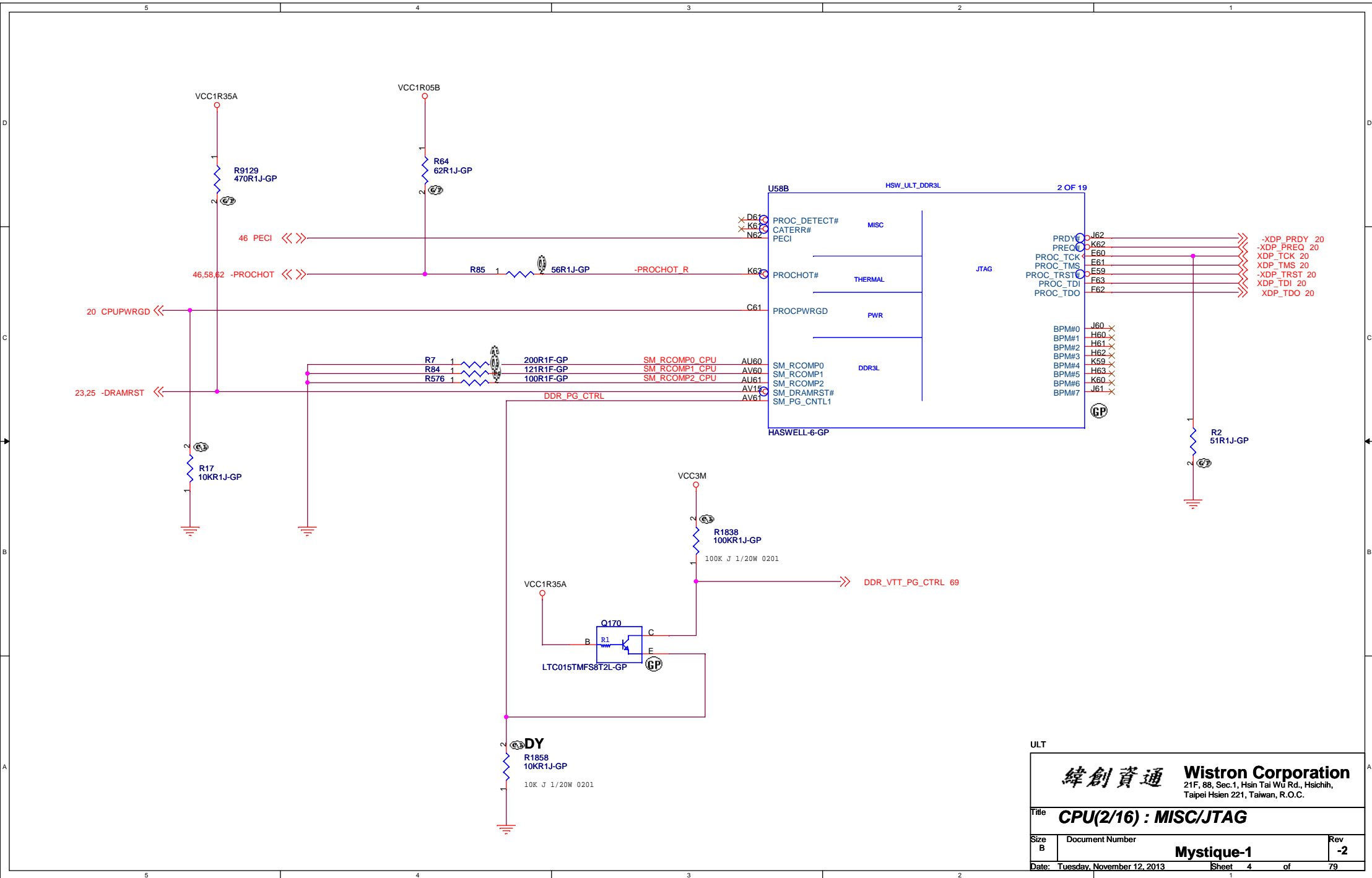
Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

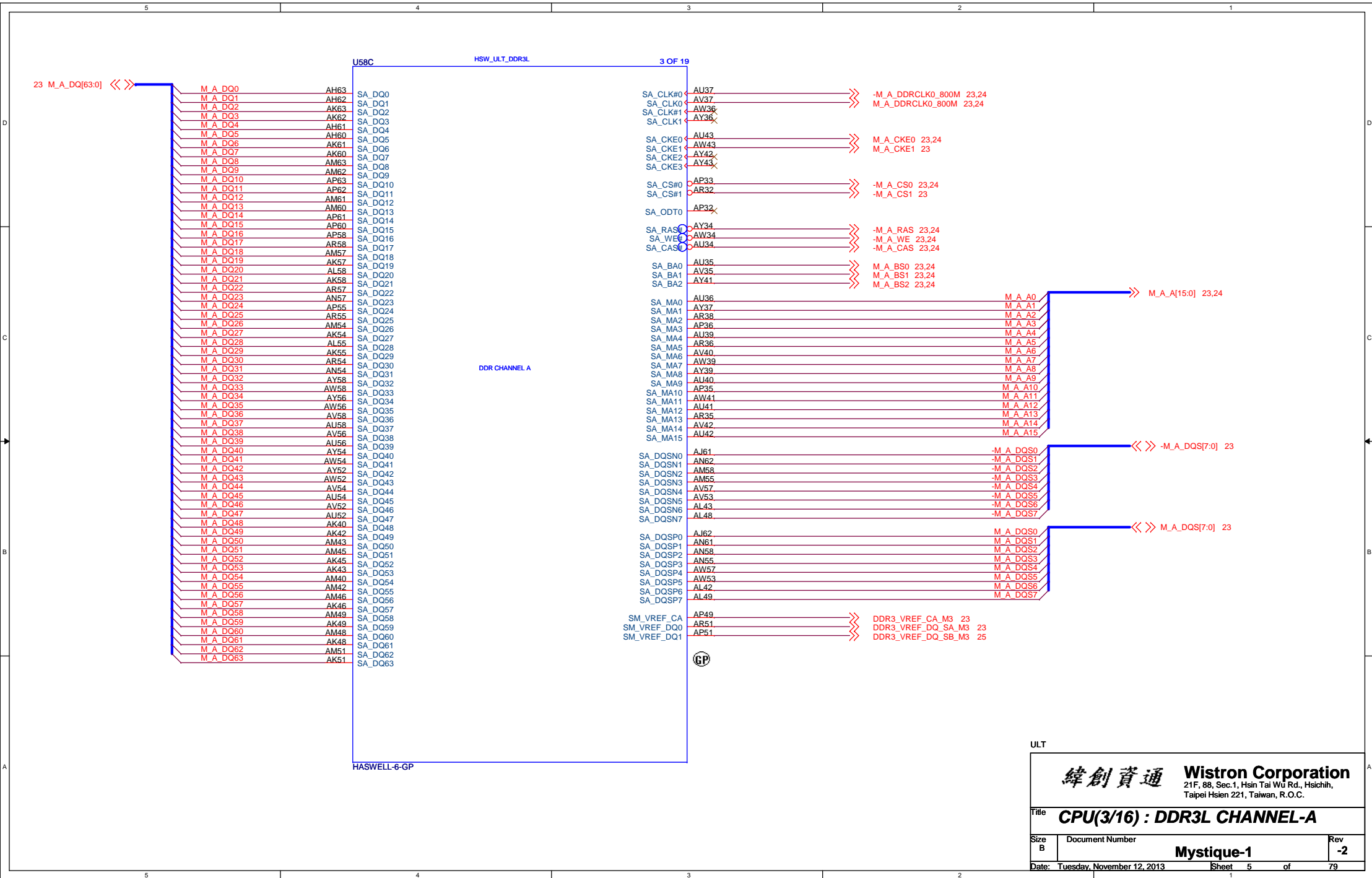
The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

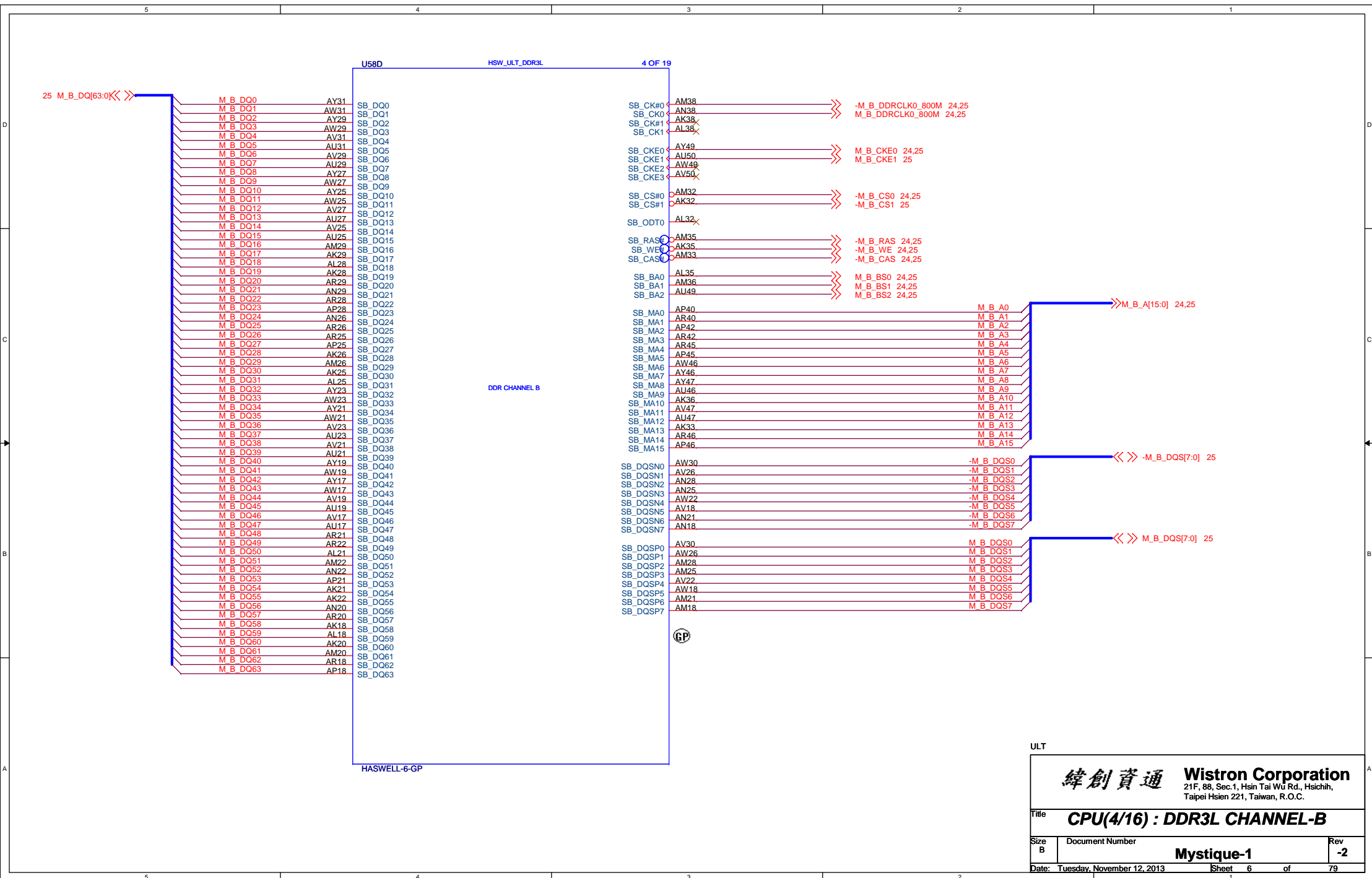




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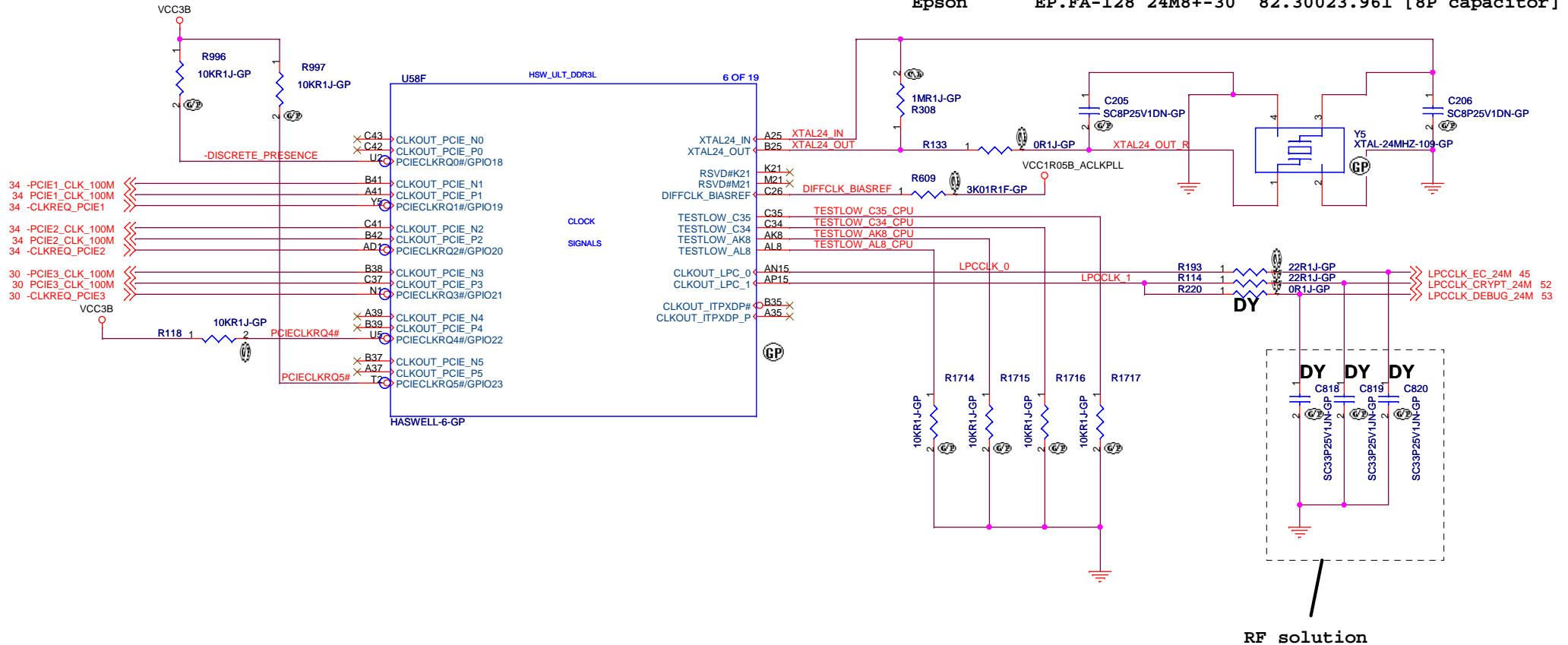
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Title CPU(3/16) : DDR3L CHANNEL-A		
Size B	Document Number	Rev -2
Mystique-1		
Date: Tuesday, November 12, 2013	Sheet 5 of	79





Y5
KDS 1ZZCAA24000CC0B 82.30023.991 [8P capacitor]
TXC 8Y24080002 82.30023.981 [8P capacitor]
Epson EP.FA-128 24M8+-30 82.30023.961 [8P capacitor]



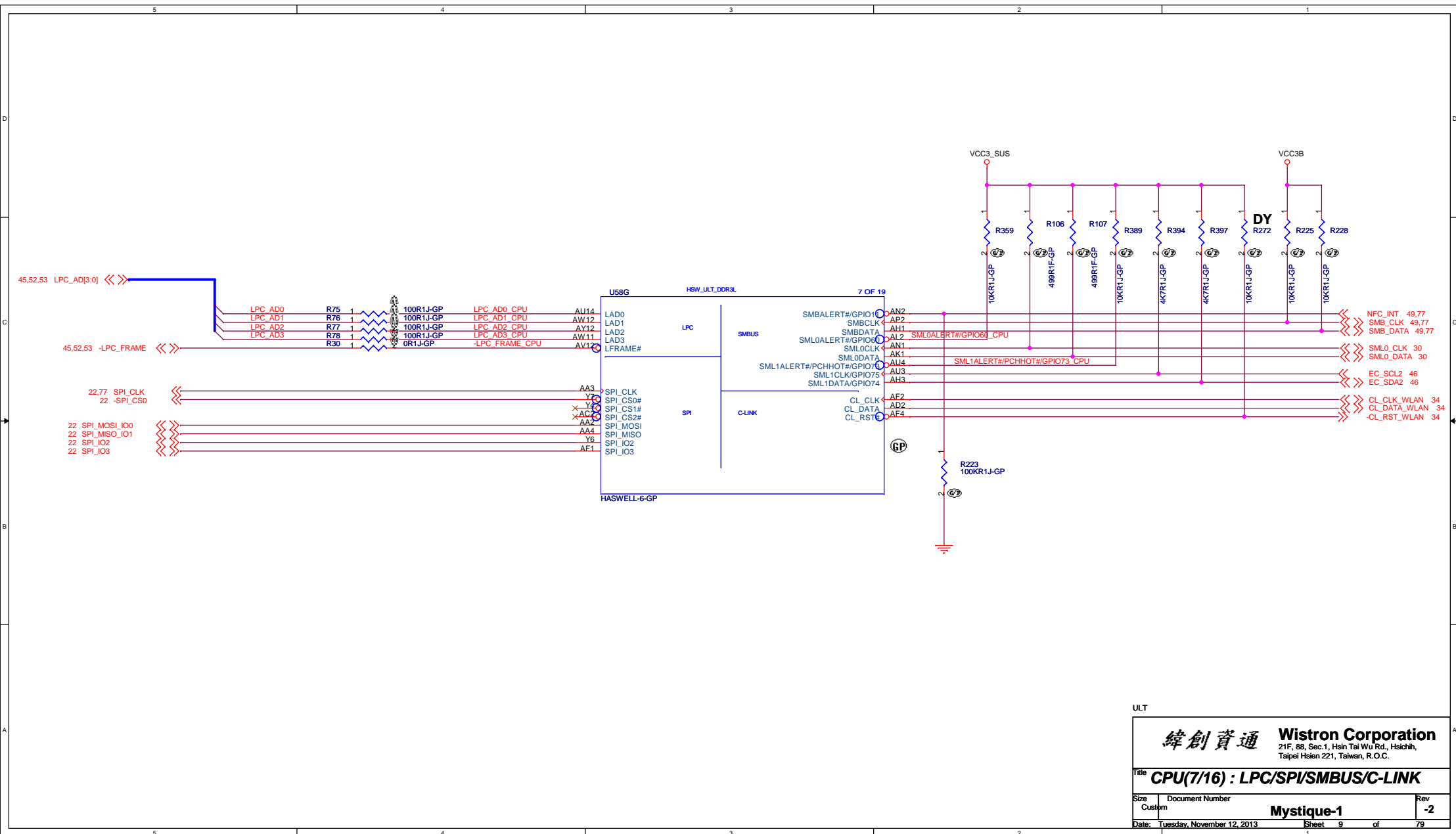
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Title CPU(6/16) : CLOCK SIGNALS

Size B Document Number Mystique-1 Rev -2

Date: Tuesday, November 12, 2013 Sheet 8 of 79



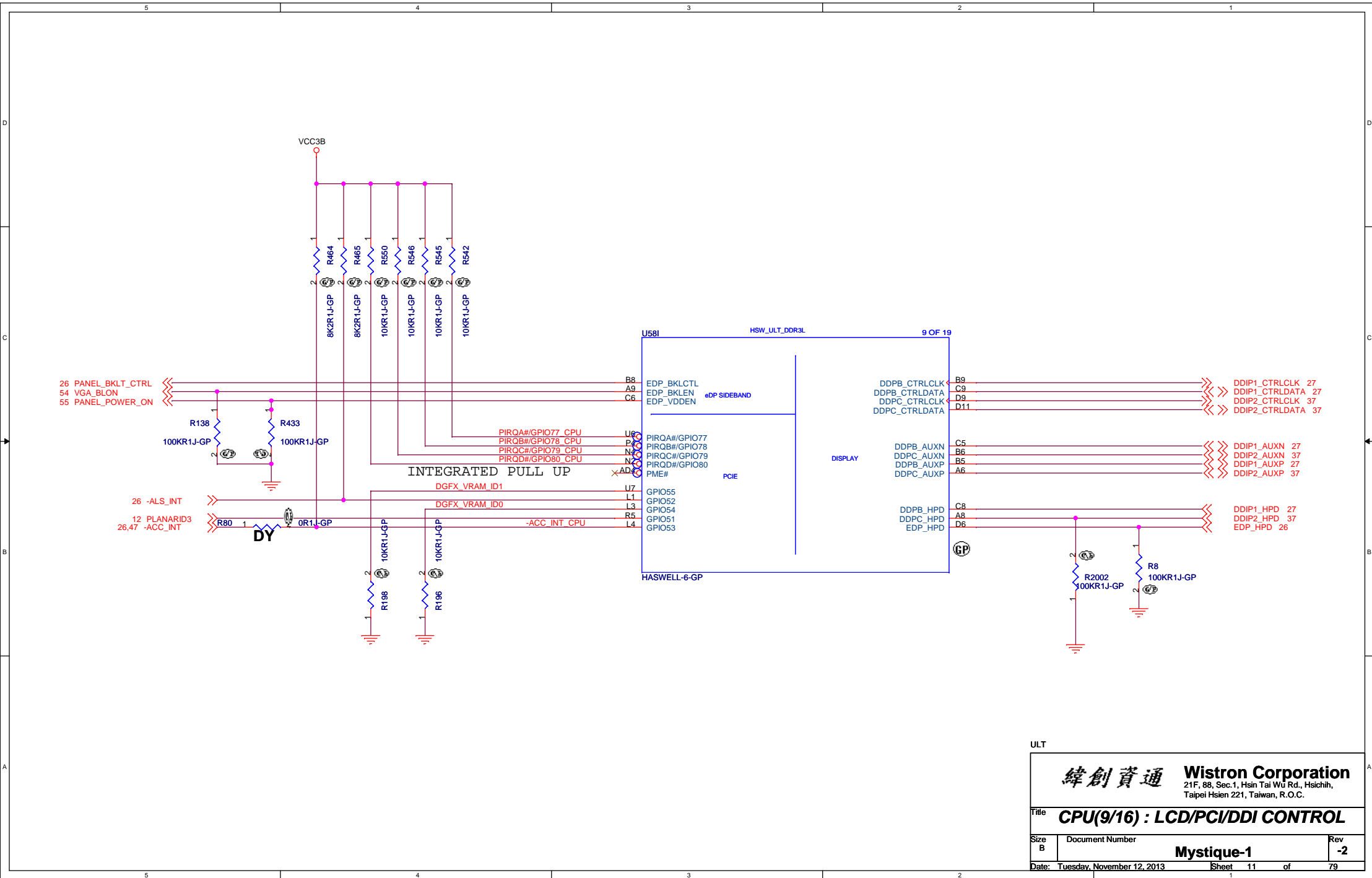
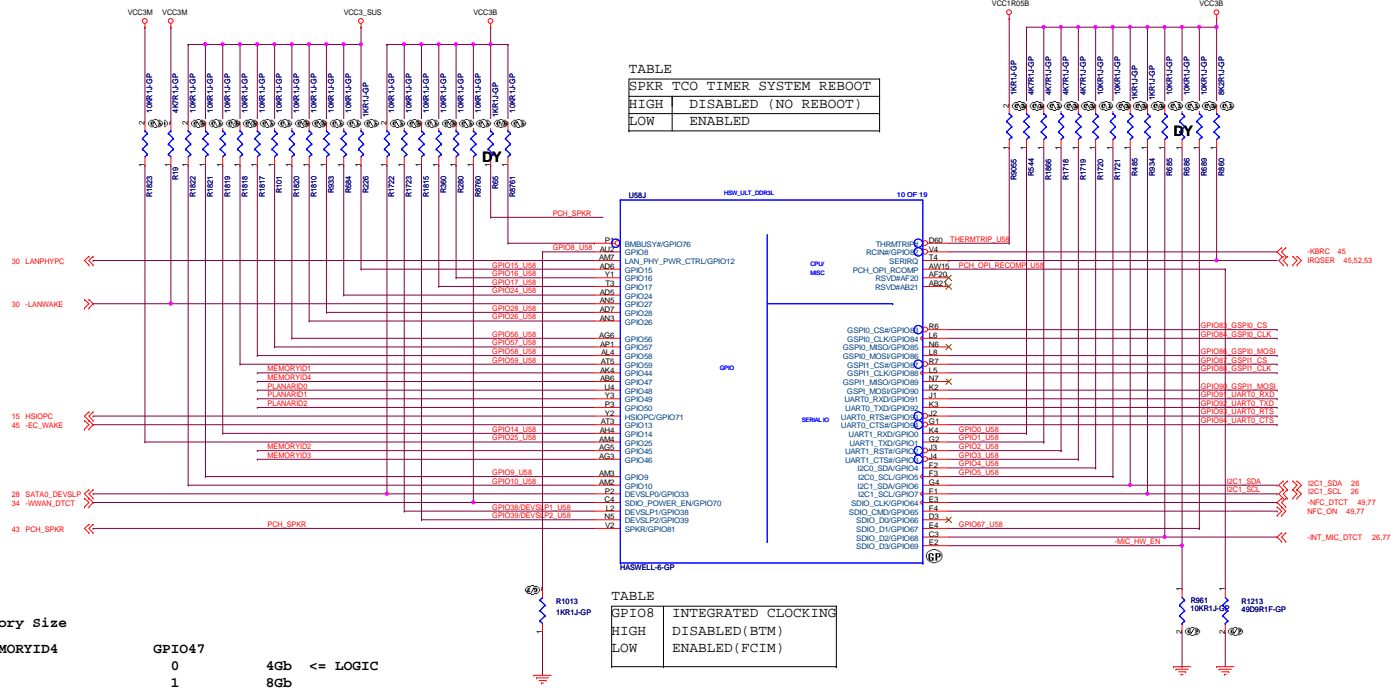
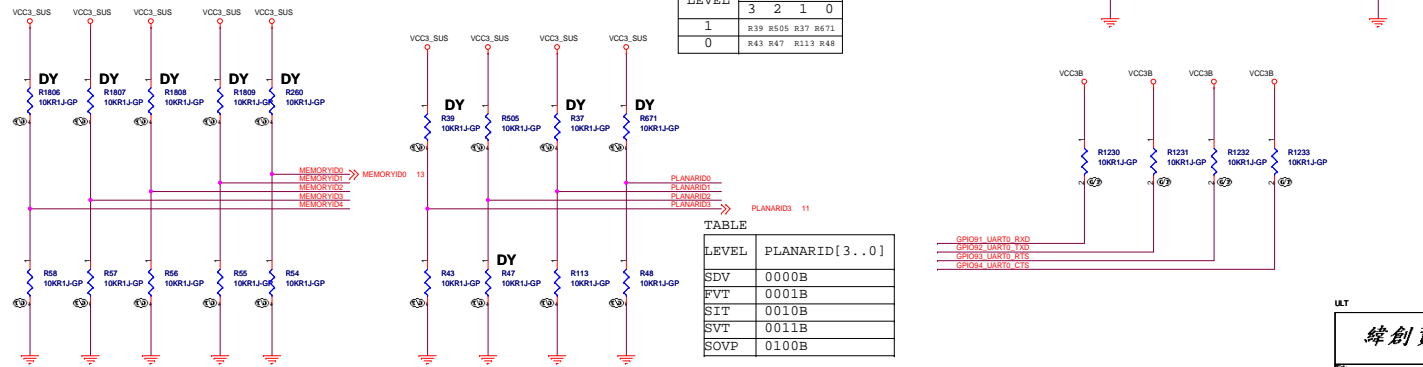
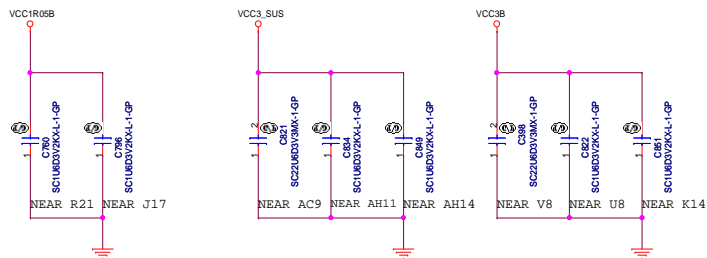


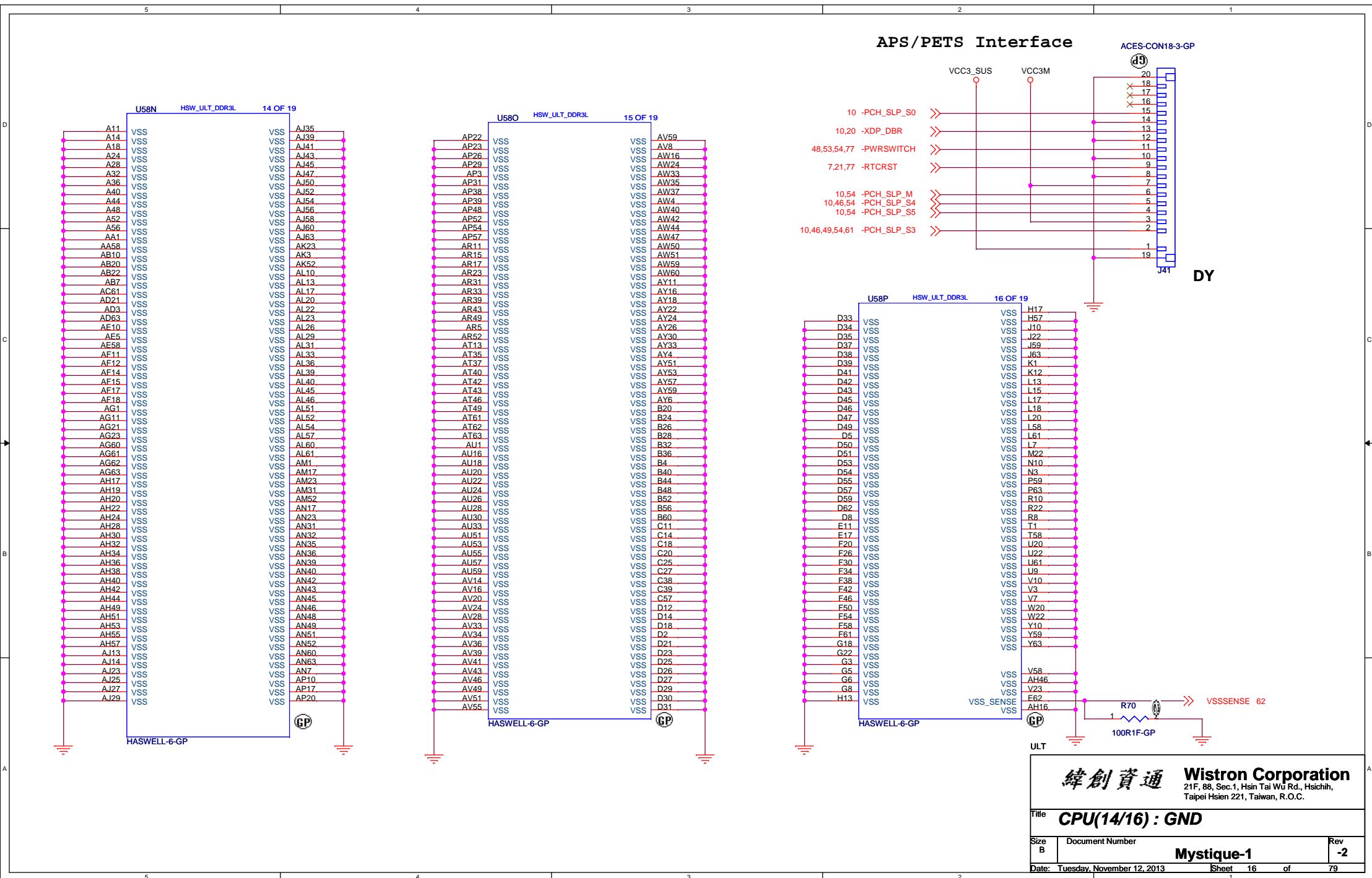
TABLE	
GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY



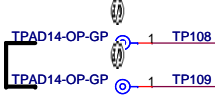
Meory Size	GPIO47	
MEMORYID4	0	4Gb <= LOGIC
	1	8Gb
Meory Supplier	GPIO[46:44]	
MEMORYID[3:1]	000	ELPIDA <= LOGIC
	001	HYNIX
	010	SAMSUNG
	011	MICRON
	[100:111]	(reserved)
Meory Die Revision	GPIO43	
MEMORYID0	0	initial <= LOGIC
	1	revised







FOR SOLDER CRACK DETECTION



- DC TEST AY2 AW2 AY2
- DC TEST AY3 AW3 AY3
- TP DC TEST AY60 AY80
- DC TEST AY61 AW61 AY61
- DC TEST AY62 AW62 AY62
- TP DC TEST B2 B2
- DC TEST A3 B3 B3
- DC TEST A61 B61 B61
- DC TEST B62 B63 B62
- DC TEST C1 C2 C1
- C2

U58Q HSW_ULT_DDR3L 17 OF 19

DAISY_CHAIN_NCTF_AY2

DAISY_CHAIN_NCTF_AY3

DAISY_CHAIN_NCTF_AY60

DAISY_CHAIN_NCTF_AY61

DAISY_CHAIN_NCTF_AY62

DAISY_CHAIN_NCTF_B2

DAISY_CHAIN_NCTF_B3

DAISY_CHAIN_NCTF_B61

DAISY_CHAIN_NCTF_B62

DAISY_CHAIN_NCTF_B63

DAISY_CHAIN_NCTF_C1

DAISY_CHAIN_NCTF_C2

HASWELL-6-GP

- DAISY_CHAIN_NCTF_A3
- DAISY_CHAIN_NCTF_A4
- DAISY_CHAIN_NCTF_A60
- DAISY_CHAIN_NCTF_A61
- DAISY_CHAIN_NCTF_A62
- DAISY_CHAIN_NCTF_AW1
- DAISY_CHAIN_NCTF_AW2
- DAISY_CHAIN_NCTF_AW3
- DAISY_CHAIN_NCTF_AW61
- DAISY_CHAIN_NCTF_AW62
- DAISY_CHAIN_NCTF_AW63

- A3 DC TEST A3 B3
- A4 TP DC TEST A4
- A60 TP DC TEST A60
- A61 DC TEST A61 B61
- A62 TP DC TEST A62
- AW1 TP DC TEST AW1
- AW2 DC TEST AY2 AW2
- AW3 DC TEST AY3 AW3
- AW61 DC TEST AY61 AW61
- AW62 DC TEST AY62 AW62
- AW63 TP DC TEST AW63

FOR SOLDER CRACK DETECTION

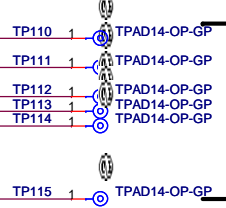
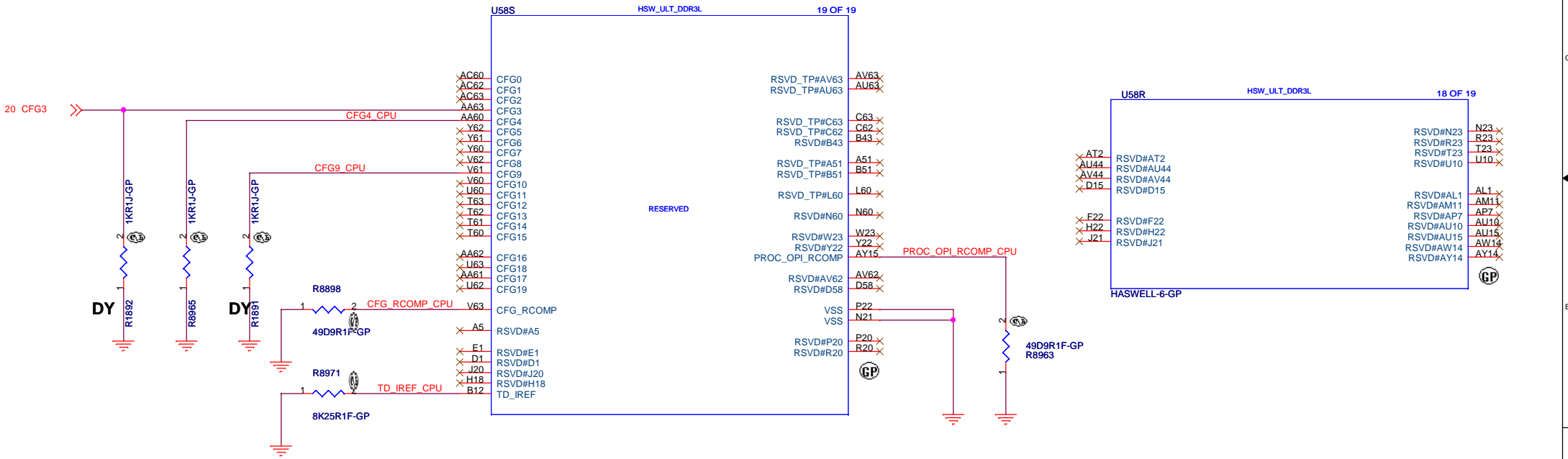


TABLE	
CFG4 : Display Port Presence	
1	: Disabled
0	: Enabled
CFG9 : SVID Bus Communication	
1	: Enabled
0	: Disabled



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Size	Document Number				Rev
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XDP

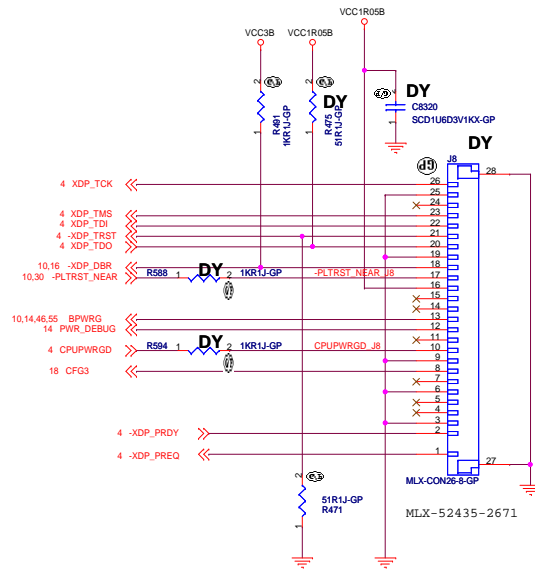


TABLE NOTE: J8 "ASM" FOR PDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
	J8	ASM	NO ASM
	C8320	ASM	NO ASM

LOGIC

JTAG

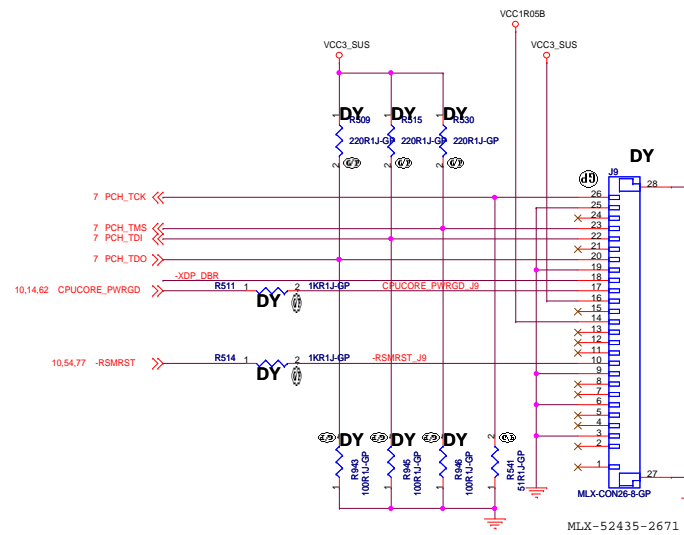


TABLE NOTE: J9 "ASM" FOR PDV ONLY.

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509 R943	220 100	NO ASM NO ASM
TMS	R530 R946	220 100	NO ASM NO ASM
TDI	R515 R945	220 100	NO ASM NO ASM
TCK	R541	51	51
CPUCORE_PWRGD	R511	ASM	NO ASM
-RSMRST	R514	ASM	NO ASM
	J9	ASM	NO ASM

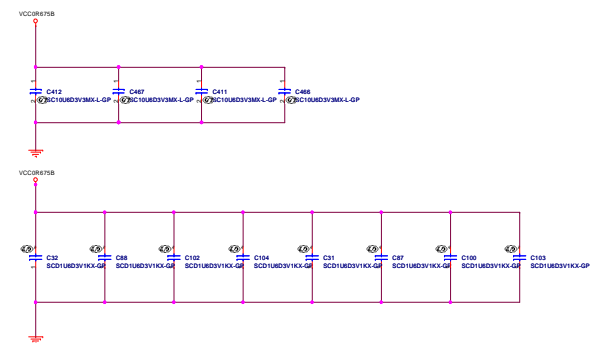
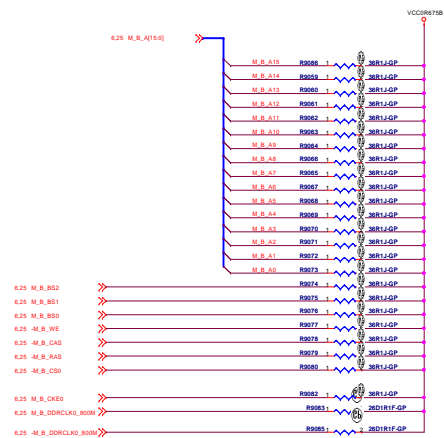
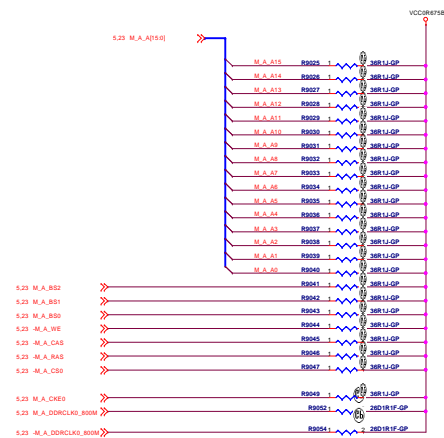
LOGIC

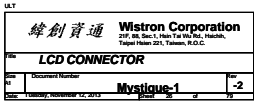
ULT

[illegible]

SF100	PIN	HEADER	INTERFACE	(TOP	VIEW
1	VCC	D12.1	GND	GND	2
3	CS#	R322.2	R681.2	CLK	4
5	MISO	R694.2	R674.2	MOSI	6
7	(KEY)	N/A	N/A	(RESET)	8

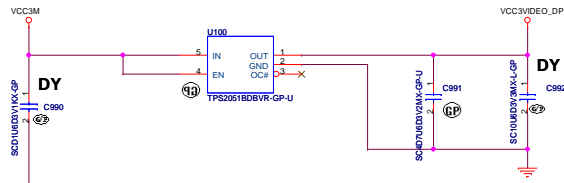
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16MB  SOIC8
WINBOND  W25Q128FVSIQ      72.25128.0E1
MACRONIX MX25L12875FM2I-10G 72.12873.001
MICRON   N25Q128A13ESEC0F  72.25128.0B1
```



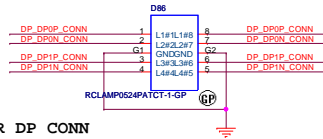
PLACE NEAR DP CONN

Typ. 500mA
Max 1.5A

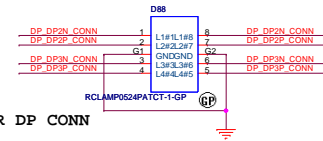


Pin#1 condition	Connection	Note
LOW	nA to nB1	w/ Dongle
HIGH	nA to nB2	w/o Dongle

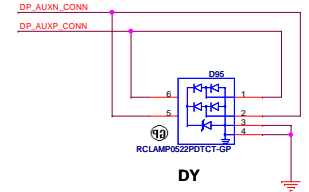
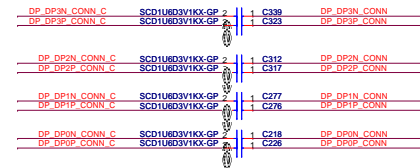
NEAR DP CONN



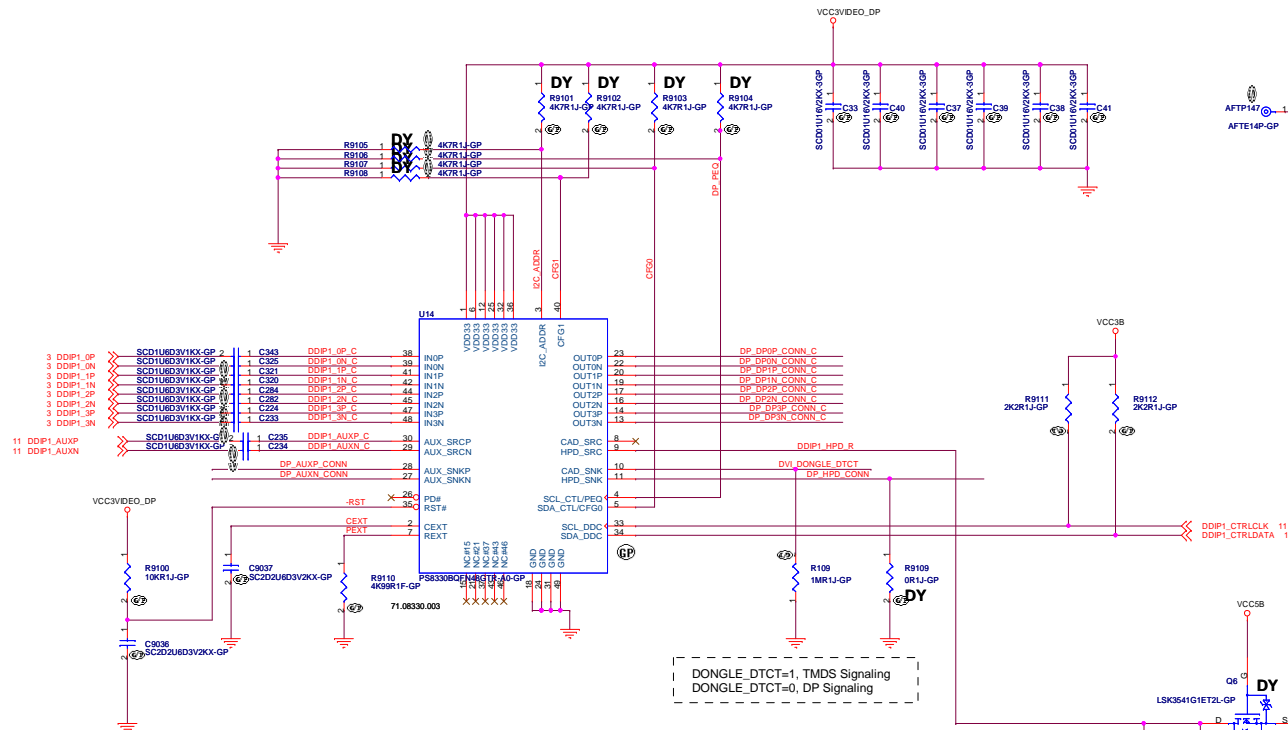
NEAR DP CONN



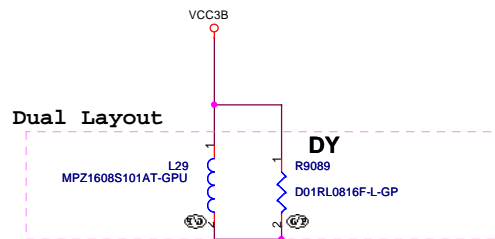
FOR SYSTEM DP NEAR DP CONN



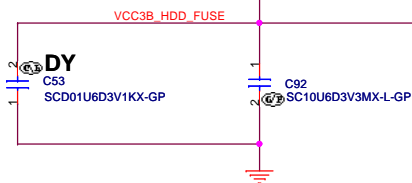
Add Demux for DP in Cable Dock Case



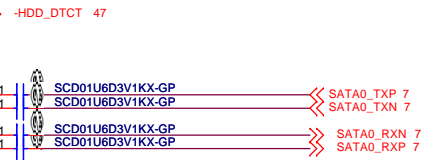
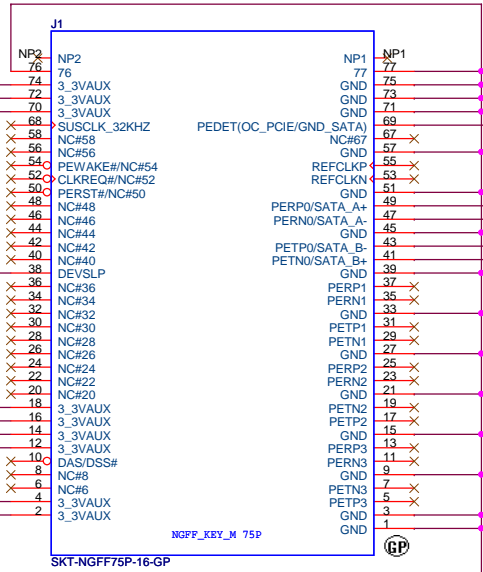
DONGLE_DTCT=1, TMDS Signaling
DONGLE_DTCT=0, DP Signaling

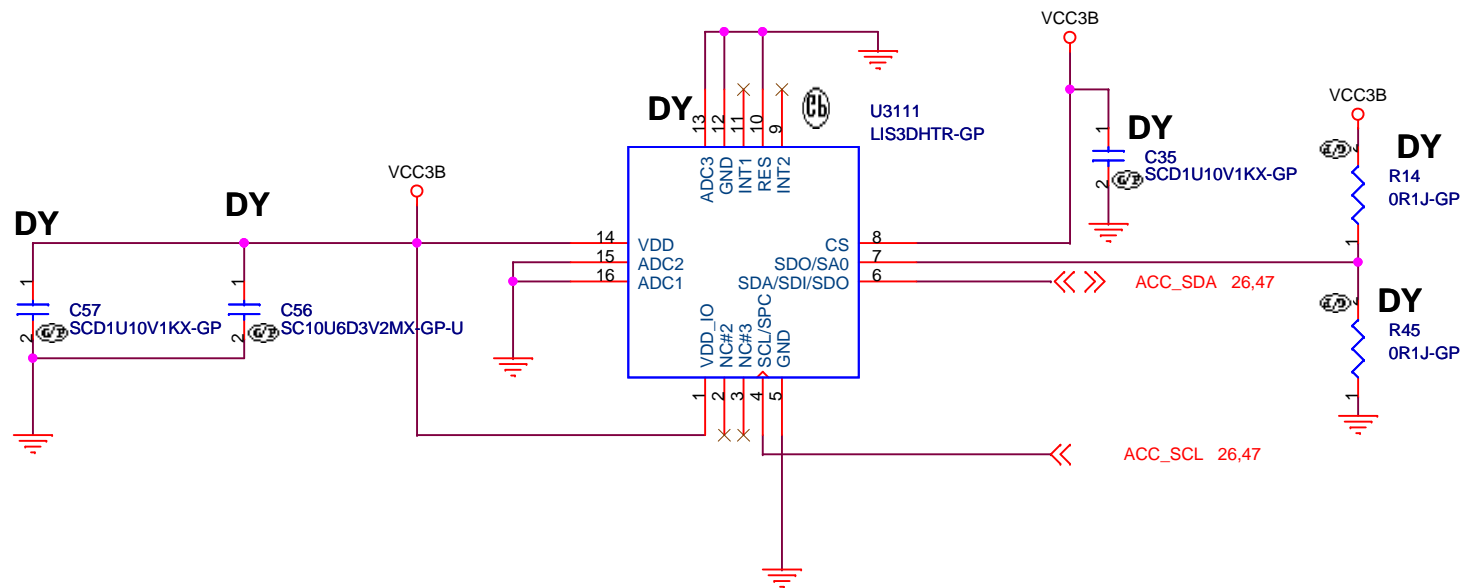


M.2 SSD Type-M 2280



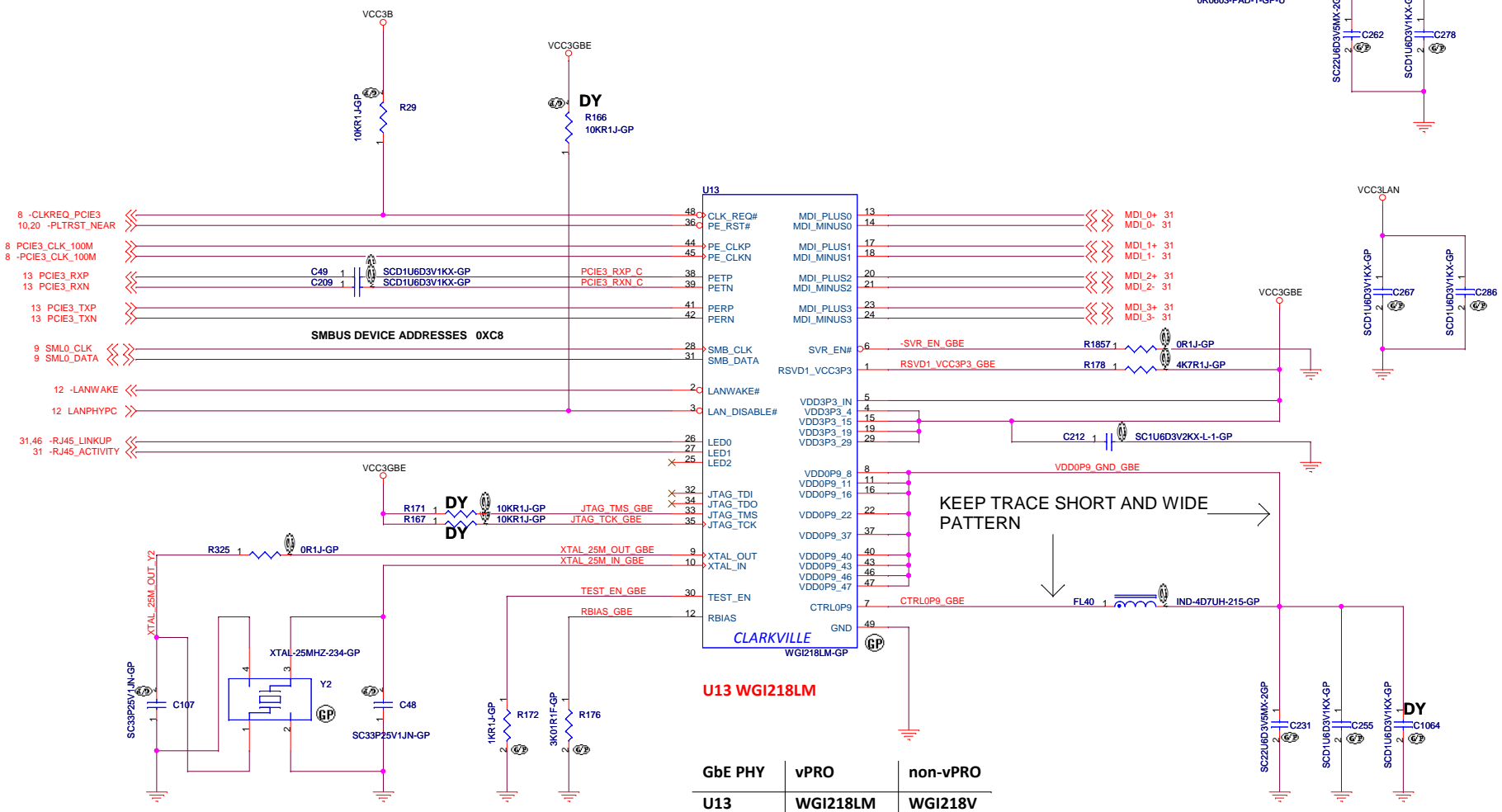
12 SATA0_DEVSLP >>> R100 1 >>> 0R1J-GP >>> SATA0_DEVSLP J1





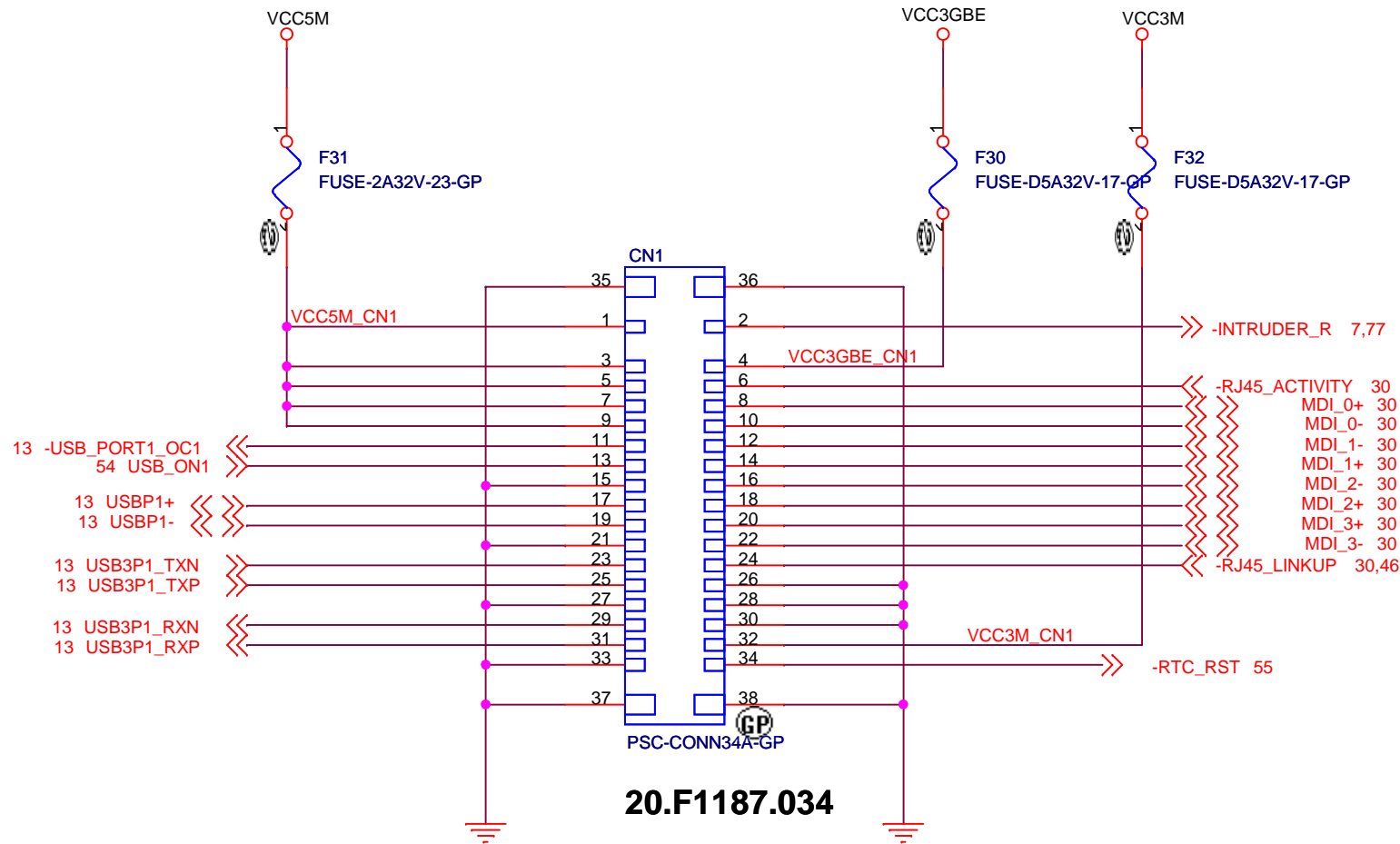
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Title G-SENSOR					
Size A4	Document Number				Rev -2
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Y2
KDS 1ZZCAA25000CC0C 82.30020.N91 [12P capacitor]
TXC 8Y25000004 82.20026.191 [33P capacitor]
Epson EP.FA-128 25M20+-30 82.30020.P51 [33P capacitor]

GbE PHY	vPRO	non-vPRO
U13	WGI218LM	WGI218V



ULT

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Title **GBE / USB3.0 BTB CONNECTOR**

Size A Document Number

Mystique-1

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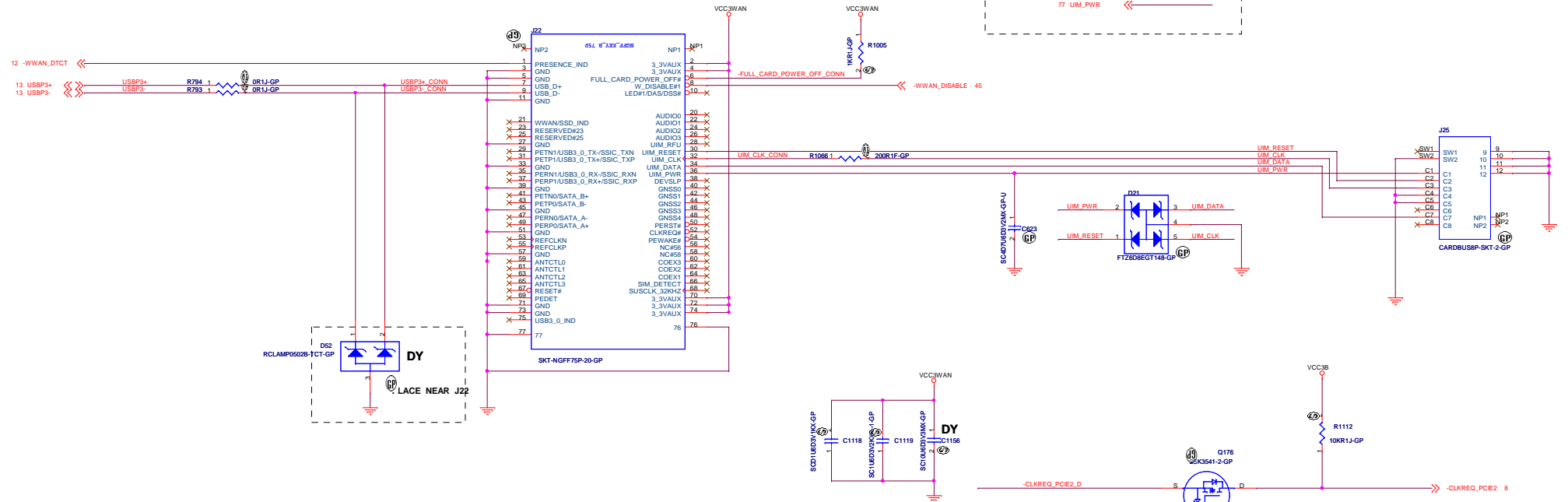
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Size A4	Document Number Mystique-1		Rev -2
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TYPE-B M.2 CARD FOR WWAN/SSD

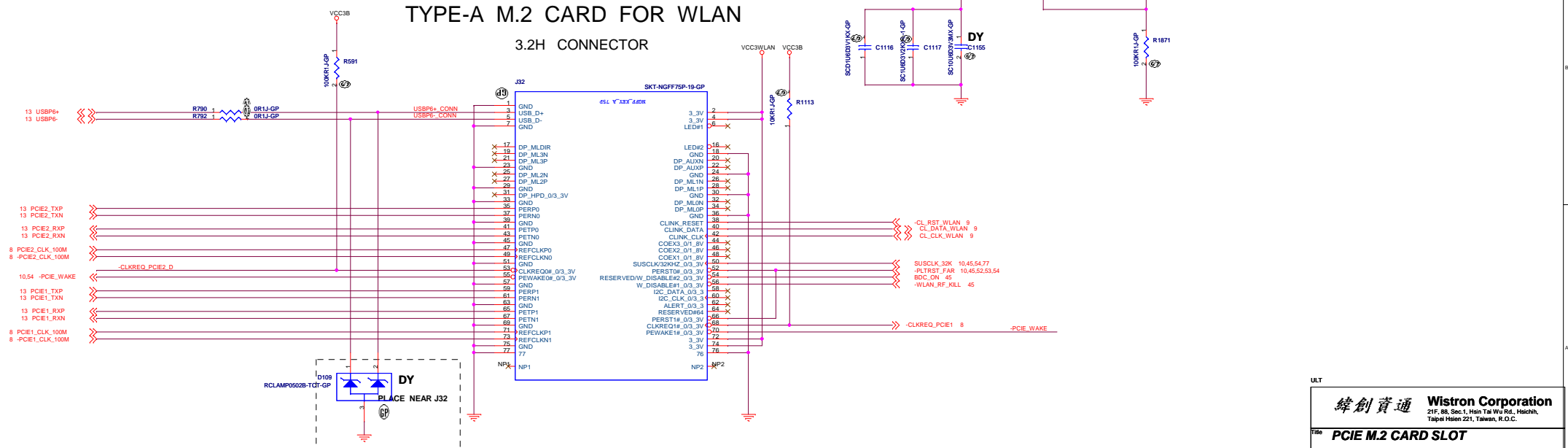
3.2H CONNECTOR

for test point, page 77.



TYPE-A M.2 CARD FOR WLAN

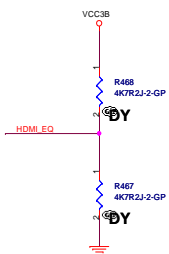
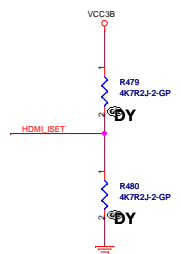
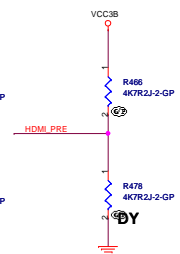
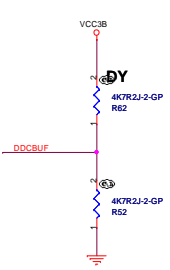
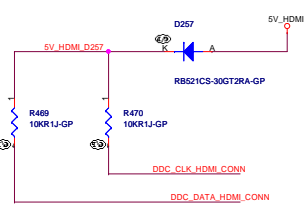
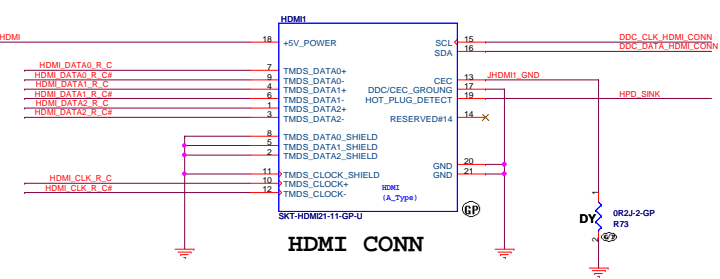
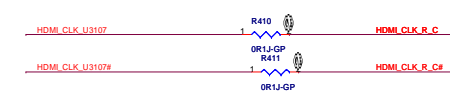
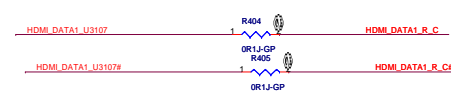
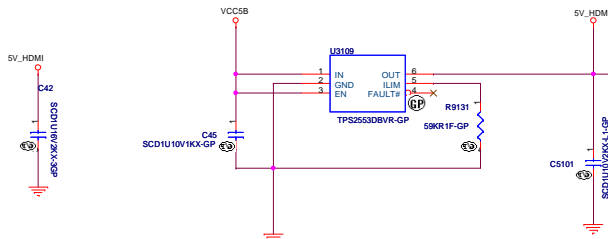
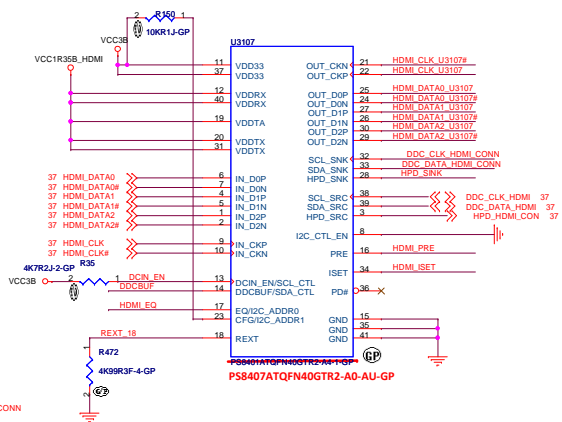
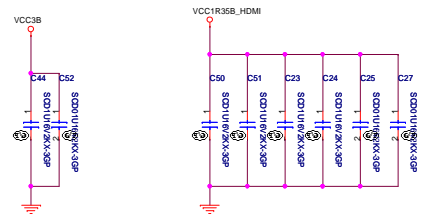
3.2H CONNECTOR



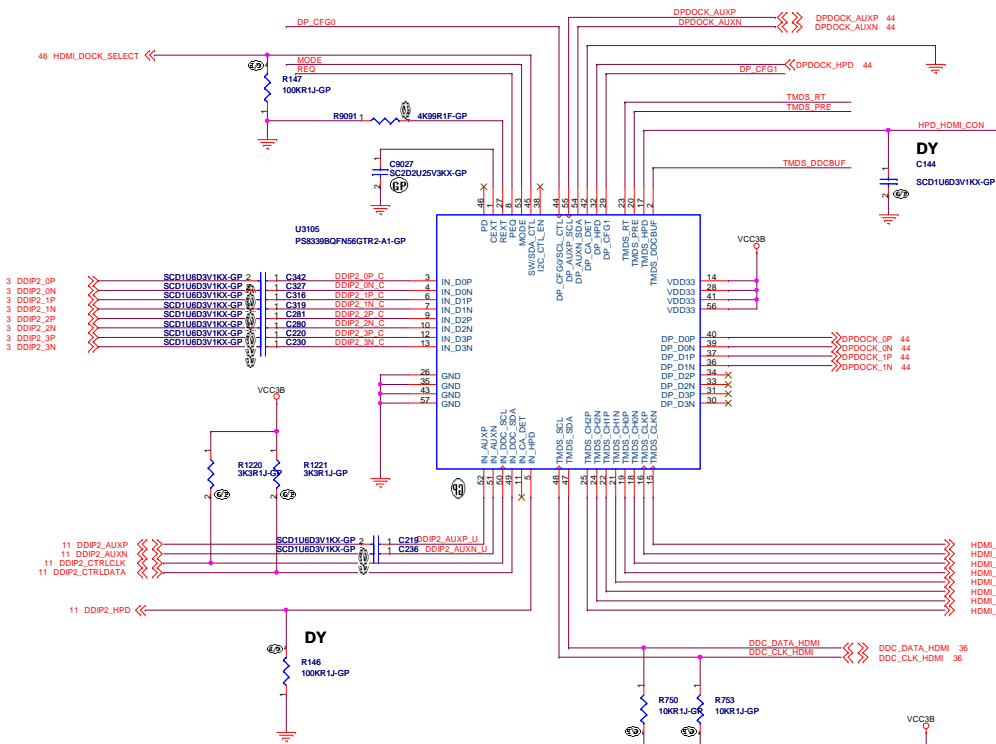
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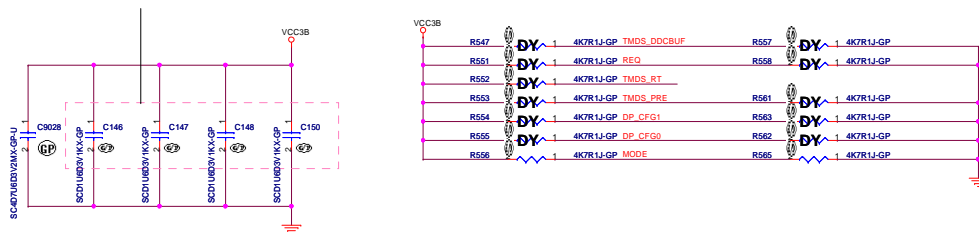
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Title BLANK		
Size A4	Document Number Mystique-1	Rev -2
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HDMI_DOCK_SELECT
H: HDMI is higher priority than DOCKDP
L: DOCKDP is higher priority than HDMI



0.1uF caps are near of each VDD33 pin



TMDS_PRE1 = L: no pre-emphasis
= H: 1.5dB pre-emphasis
= M: 3.0dB pre-emphasis
The pin internal pull down at ~150K, 3.3V I/O.

TMDS_RT1 = L: Standard open drain driver
= H: Open drain driver with termination resistors
The pin internal pull down at ~150K, 3.3V I/O.

TMDS_DDCBUF1 = L: DDC pass through
= H: DDC active buffer
= M: DDC pass through with 40 kohm pull up resistor
The pin internal pull down at ~150K, 3.3V I/O.

PEO1 = L: default, LEO, compensate channel loss up to 12dB @ HBR2
= H: LEO, compensate channel loss up to 15dB @ HBR2
= M: LLEQ, compensate channel loss up to 5dB @ HBR2
The pin internal pull down at ~150K, 3.3V I/O.

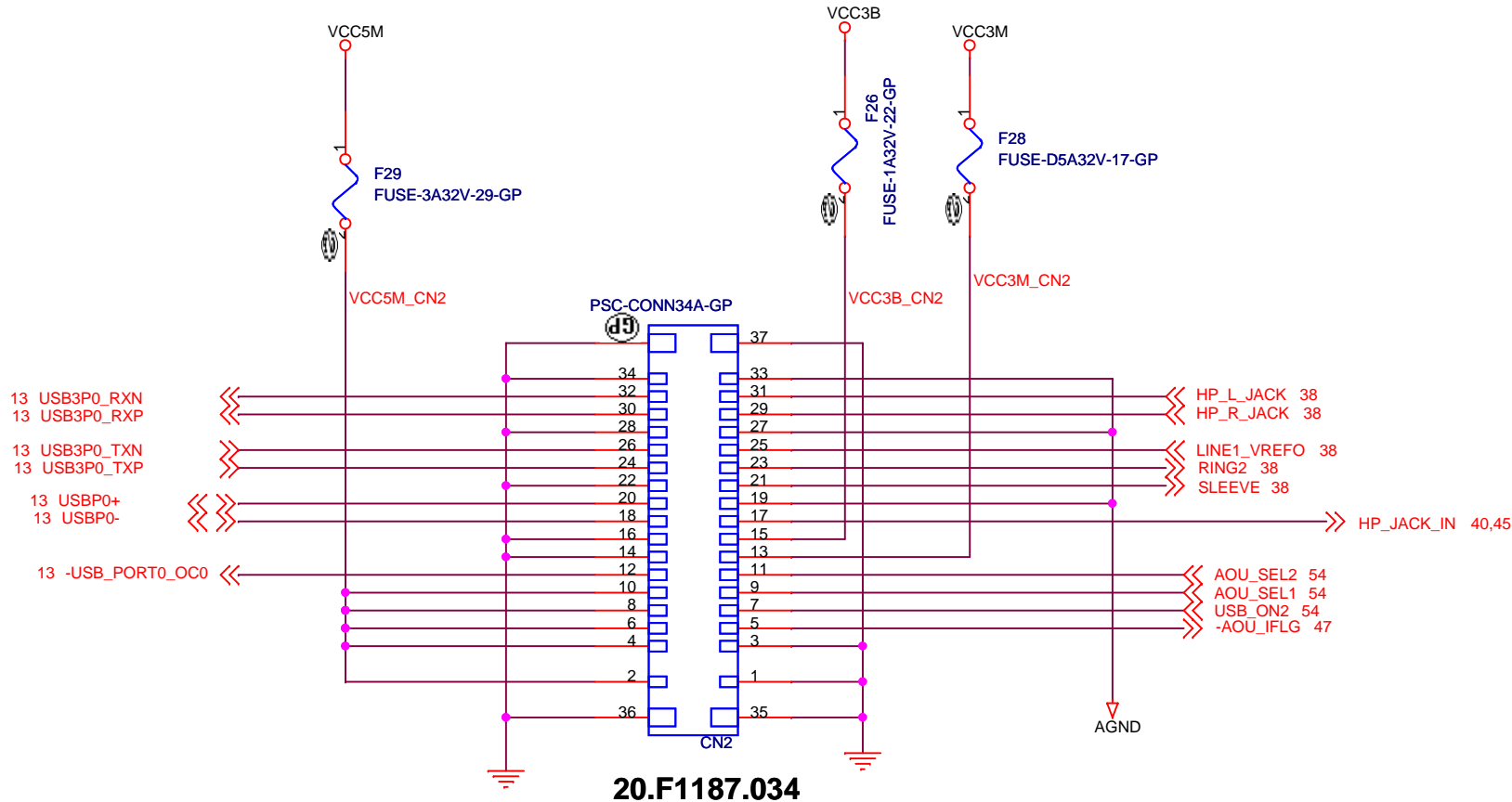
DP1_CFG1 = L: default, auto test disable & input offset cancellation enable
= H: auto test enable & input offset cancellation enable
= M: auto test disable & input offset cancellation disable
The pin internal pull down at ~150K, 3.3V I/O.

DP1_CFG0 = L: default, automatic EQ enable & AUX interception enable
= H: automatic EQ disable & AUX interception enable
= M: automatic EQ disable & AUX interception disable, no pre-emphasis, 800mVpp swing
The pin internal pull down at ~150K, 3.3V I/O.

MODE1 = L: Control Switching Mode, HDMI ID disable
= H: Automatic Switching Mode, HDMI ID disable
= M: Automatic Switching Mode, HDMI ID enable
The pin internal pull down at ~150K, 3.3V I/O.

For Control Switching:
SW 1= L: DP output is selected
SW 1= H: TMDS output is selected

For Automatic Switching:
SW1 = L: DP output has higher priority
The pin internal pull down at ~150K, 3.3V I/O.
SW1 = H: TMDS output has higher priority



20.F1187.034

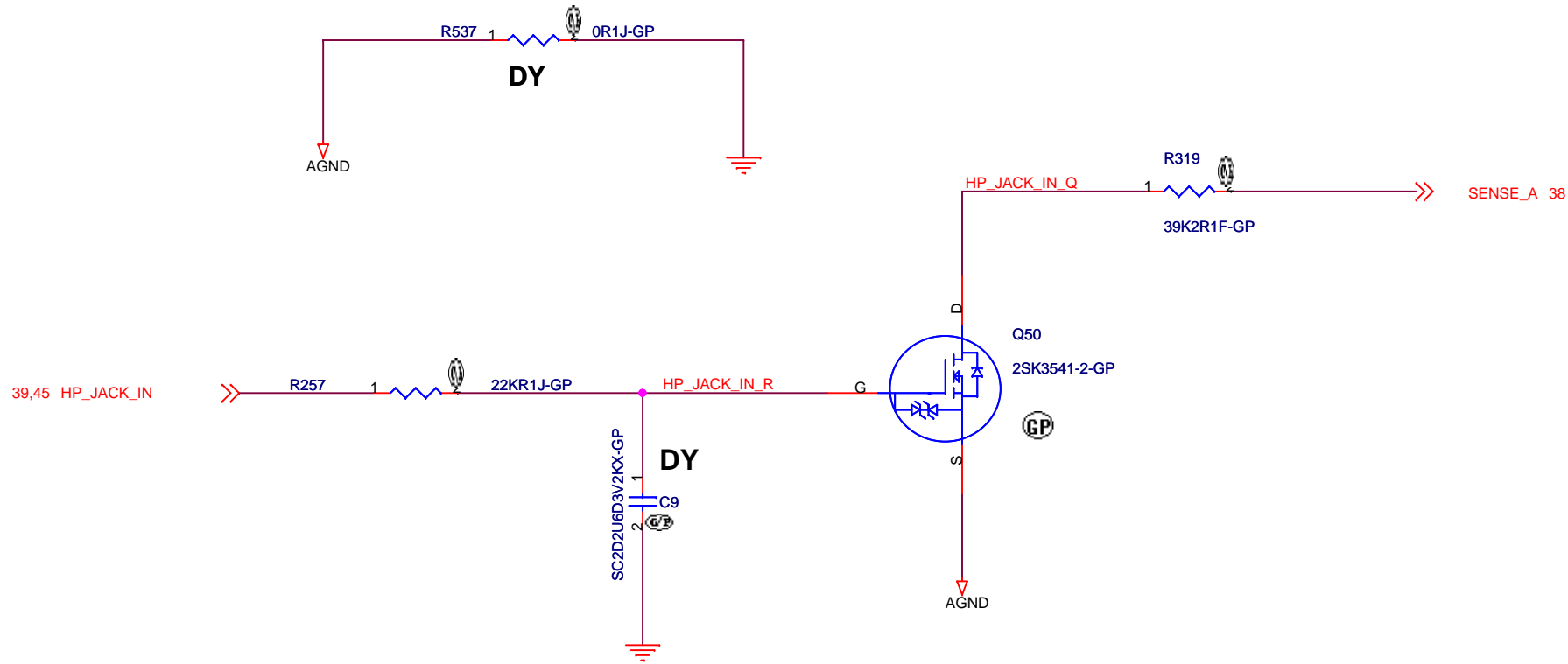
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Title **AUDIO /USB3.0 BTB CONNECTOR**

Size A4 Document Number **Mystique-1** Rev -2

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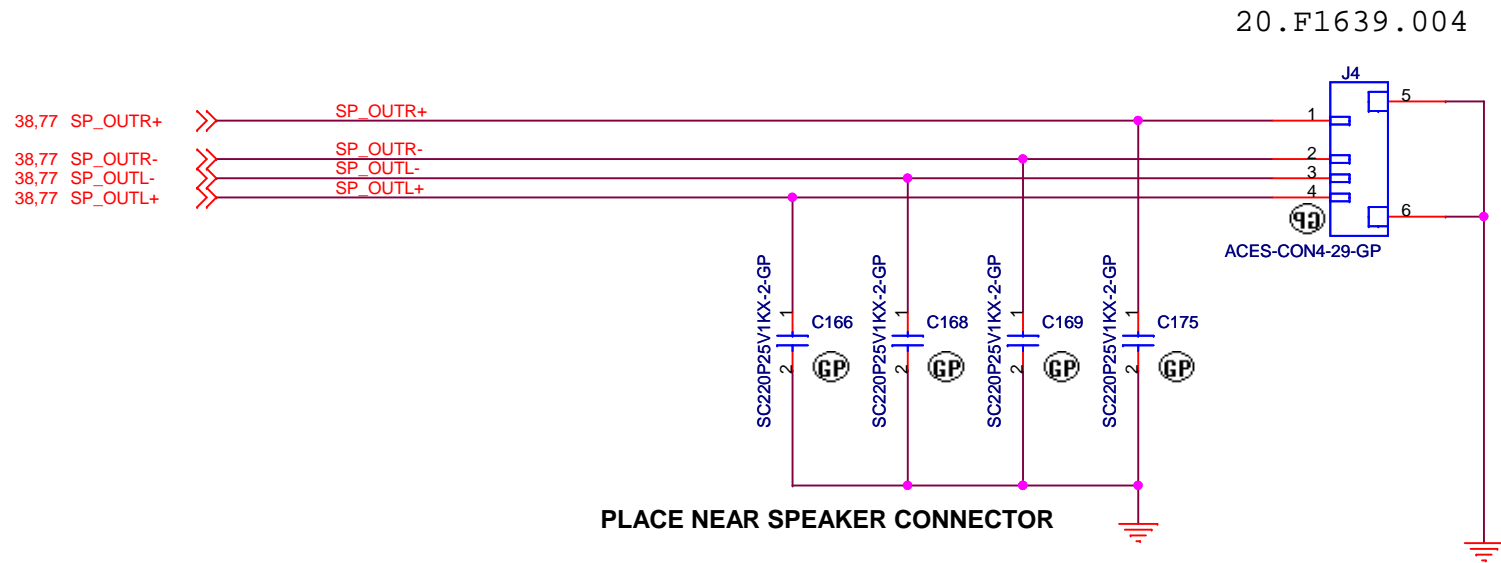
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<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title <div>AUDIO JACK SENSE</div>	
Size <div>A4</div>	<div>Document Number</div> <div>Mystique-1</div>
Date: <div>Tuesday, November 12, 2013</div>	<div>Rev</div> <div>-2</div>
Sheet <div>40</div> of <div>79</div>	

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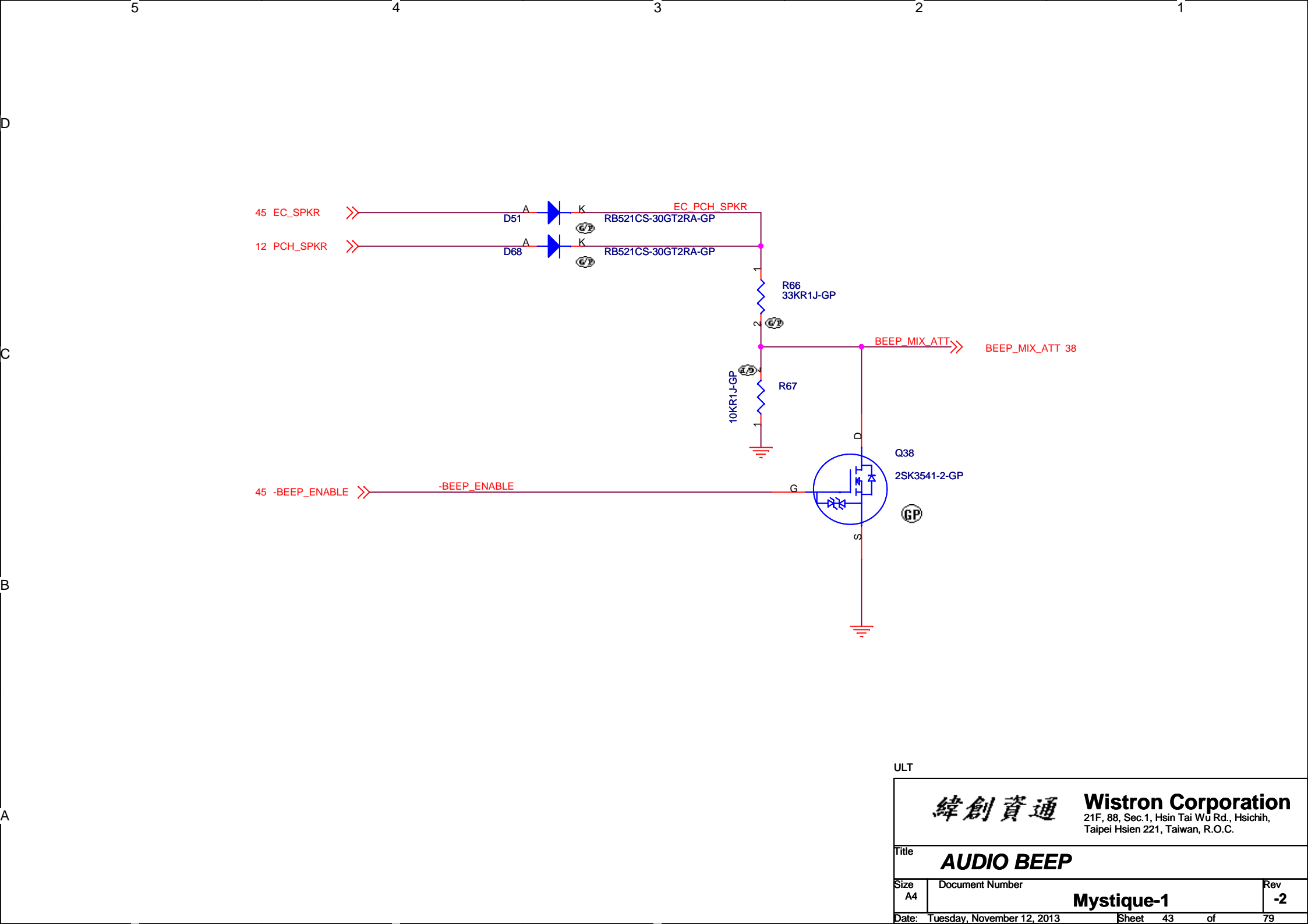
ULT

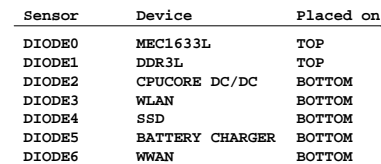
緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title BLANK			
Size B	Document Number	Mystique-1	Rev -2
Date: Tuesday, November 12, 2013	Sheet 41	of	79



ULT

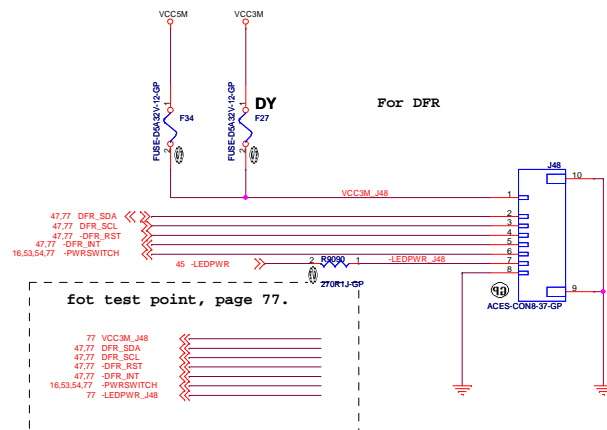
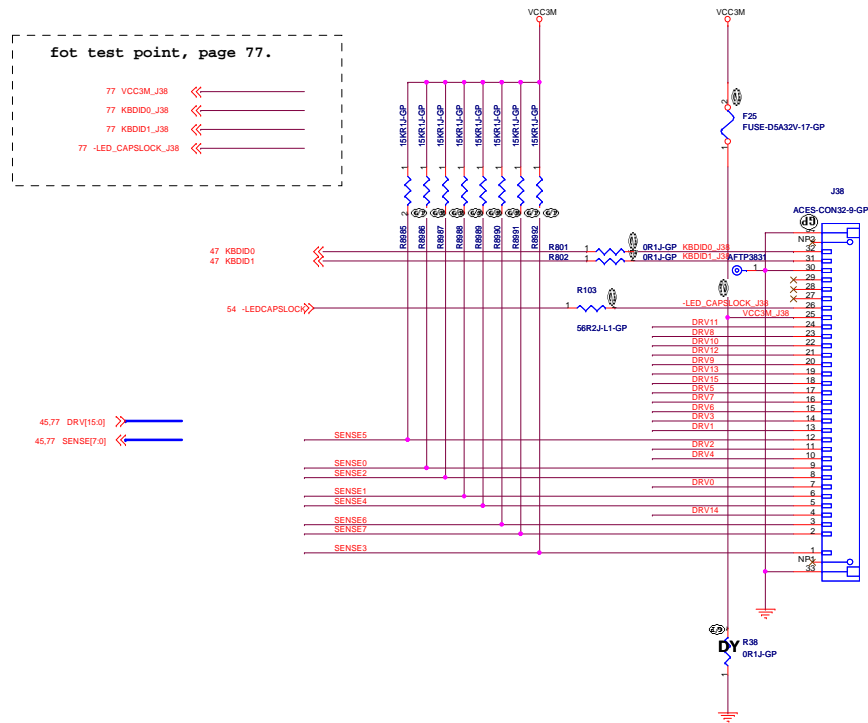
緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title AUDIO SPEAKER			
Size A4	Document Number		Rev -2
Date: Tuesday, November 12, 2013	Sheet 42	of	79



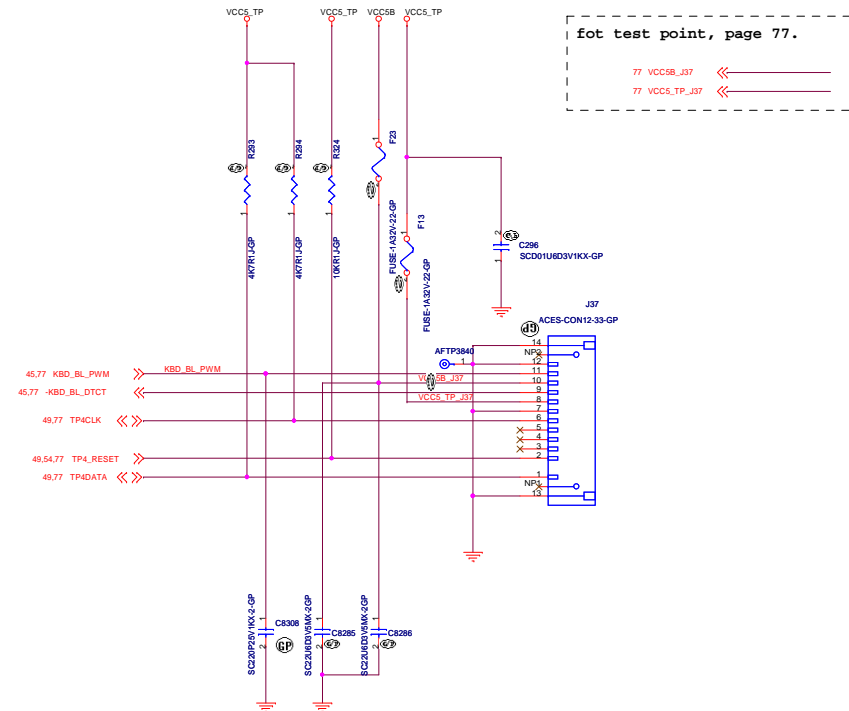




KBD(32PIN)



TRACKPOINT (12PIN)



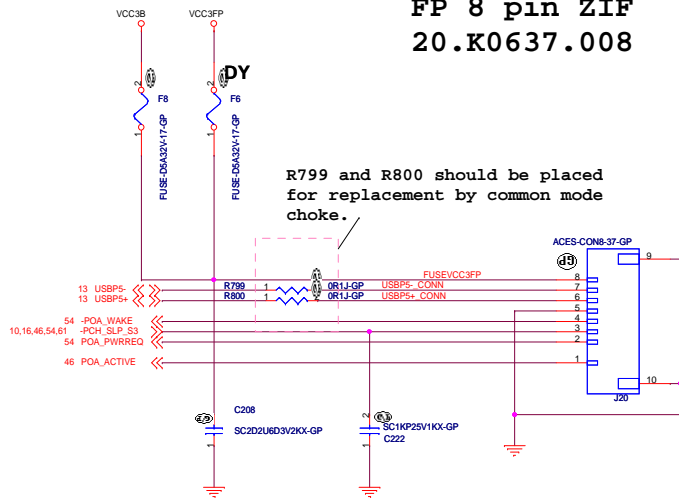
<Pin Assignment of DFR connector>

- ```
1 VCC3M OR VCC5M
2 DFR_SDA
3 DFR_SCL
4 -DFR_RST
5 -DFR_INT
6 -PWRSWITCH
7 -LED_PWR
8 GND
```



# Clickpad + NFC 20.K0637.026

FP 8 pin ZIF  
20.K0637.008



ACES-CON26-17-GP-U

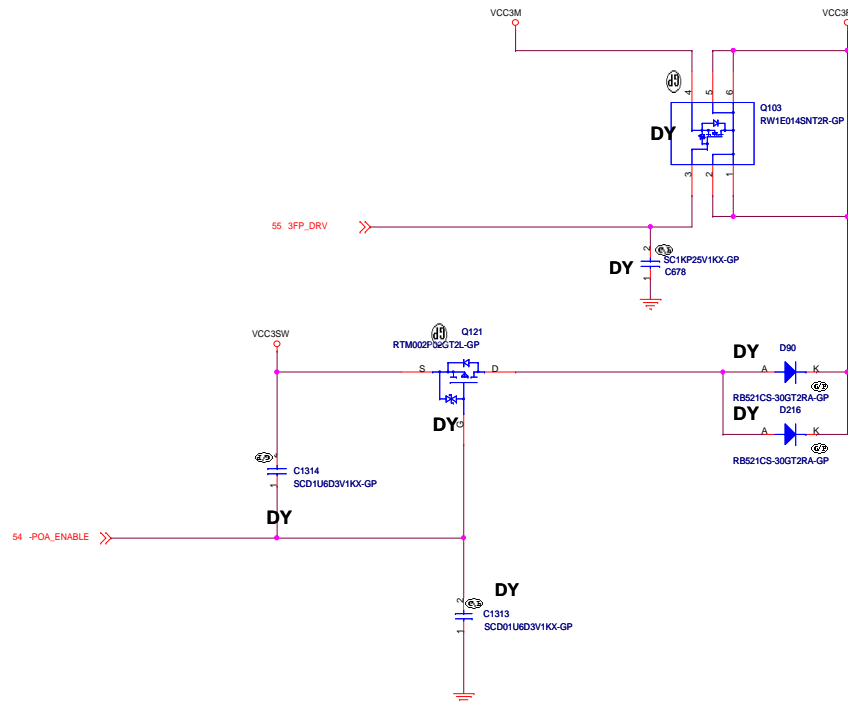
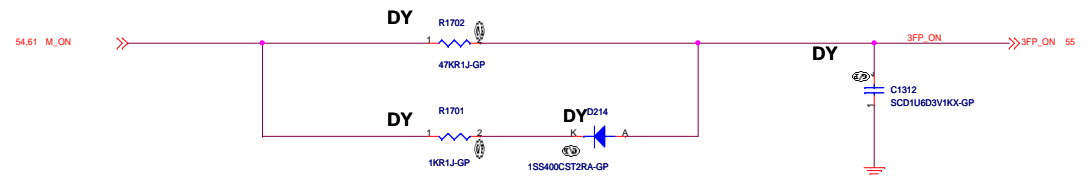


for test point, page 77.

77 FUSEVCC3B  
77 FUSEVCC5B

NFC

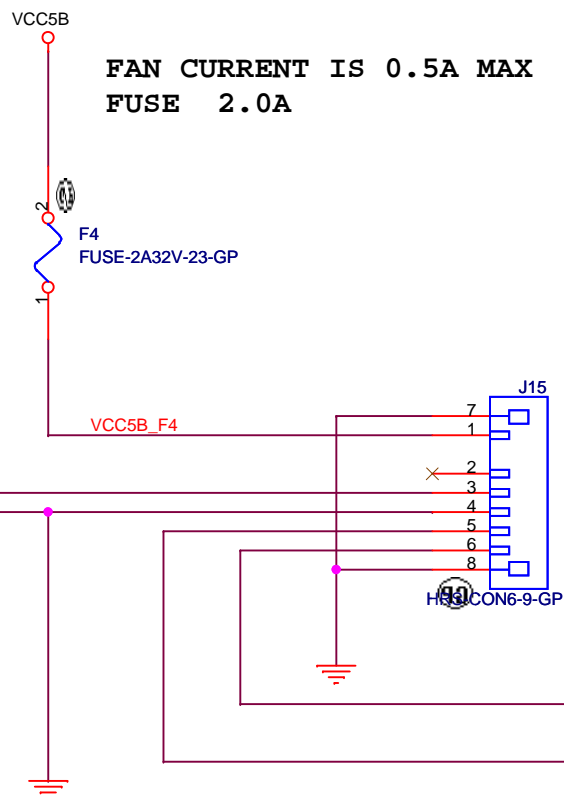
Clickpad



ULT

for test point, page 77.

77 VCC5B\_F4



## PIN# NET NAME

PIN1 VCC5B\_F4  
PIN2 NC  
PIN3 FAN\_ON  
PIN4 GND  
PIN5 FAN\_FRQ  
PIN6 FAN\_ID

FAN\_ID 46,77

FAN\_FRQ 46,77

ULT

緯創資通

**Wistron Corporation**

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Taipei Hsien 221, Taiwan, R.O.C.

Title

**FAN CONNECTOR**

Size  
A4

Document Number

**Mystique-1**

Rev  
**-2**

Date: Tuesday, November 12, 2013

Sheet 50

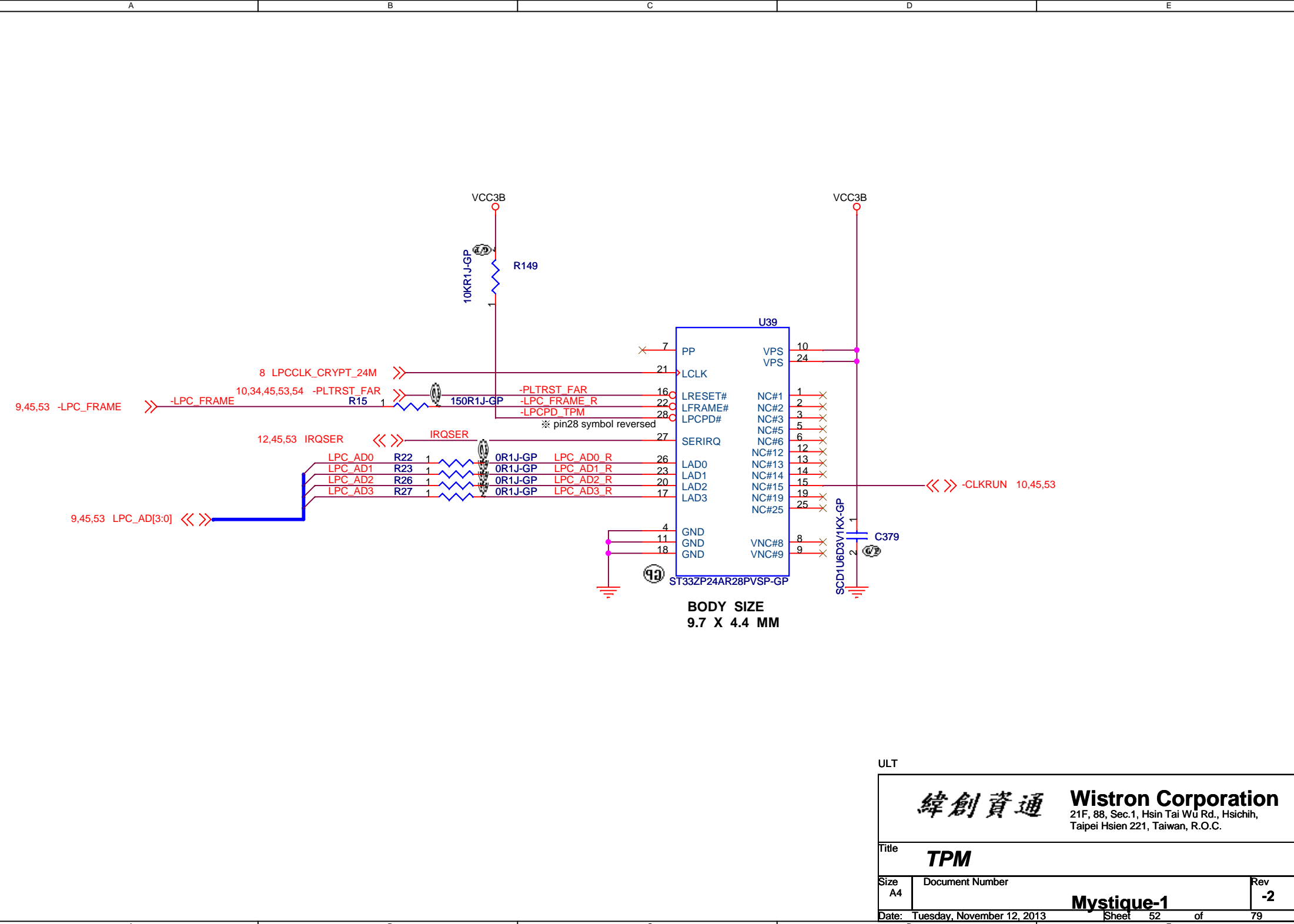
of

79

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|---------------------------------------------------------------------------------------|-----------------|-------------------------------------------------------------------------------------------------------------|-----------|
|  |                 | <b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |           |
| Title                                                                                 |                 |                                                                                                             |           |
| <b>BLANK</b>                                                                          |                 |                                                                                                             |           |
| Size<br>A4                                                                            | Document Number |                                                                                                             | Rev<br>-2 |
| Date: Tuesday, November 12, 2013                                                      |                 | Sheet 51 of                                                                                                 | 79        |



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|                                  |                 |                                                                               |     |
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| Title                            |                 |                                                                               |     |
| TPM                              |                 |                                                                               |     |
| Size                             | Document Number |                                                                               | Rev |
| A4                               | Mystique-1      |                                                                               | -2  |
| Date: Tuesday, November 12, 2013 |                 |                                                                               |     |
| Sheet 52 of 79                   |                 |                                                                               |     |

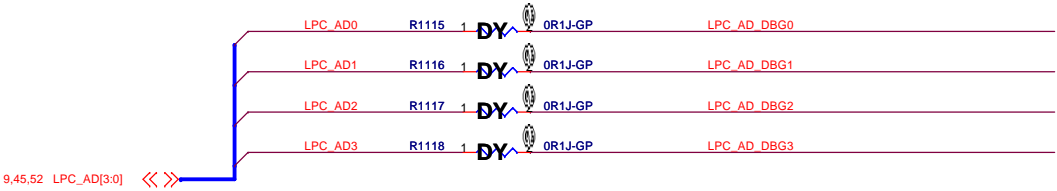
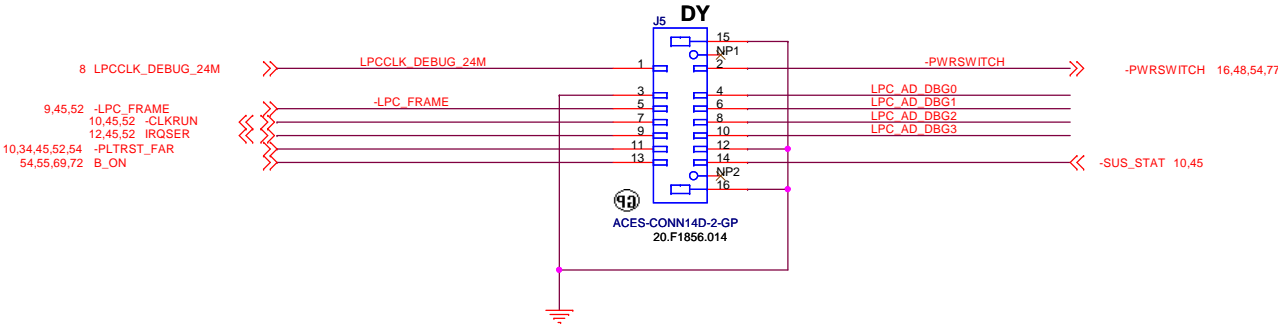
Mystique-1

TABLE

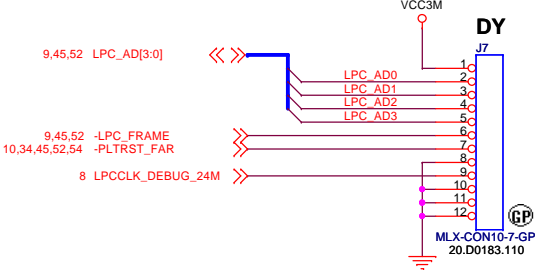
| REF DES | ENABLE | DISABLE |
|---------|--------|---------|
| J5      | ASM    | NO_ASM  |
| R220    | ASM    | NO_ASM  |
| R1115   | ASM    | NO_ASM  |
| R1116   | ASM    | NO_ASM  |
| R1117   | ASM    | NO_ASM  |
| R1118   | ASM    | NO_ASM  |

↑  
LOGIC

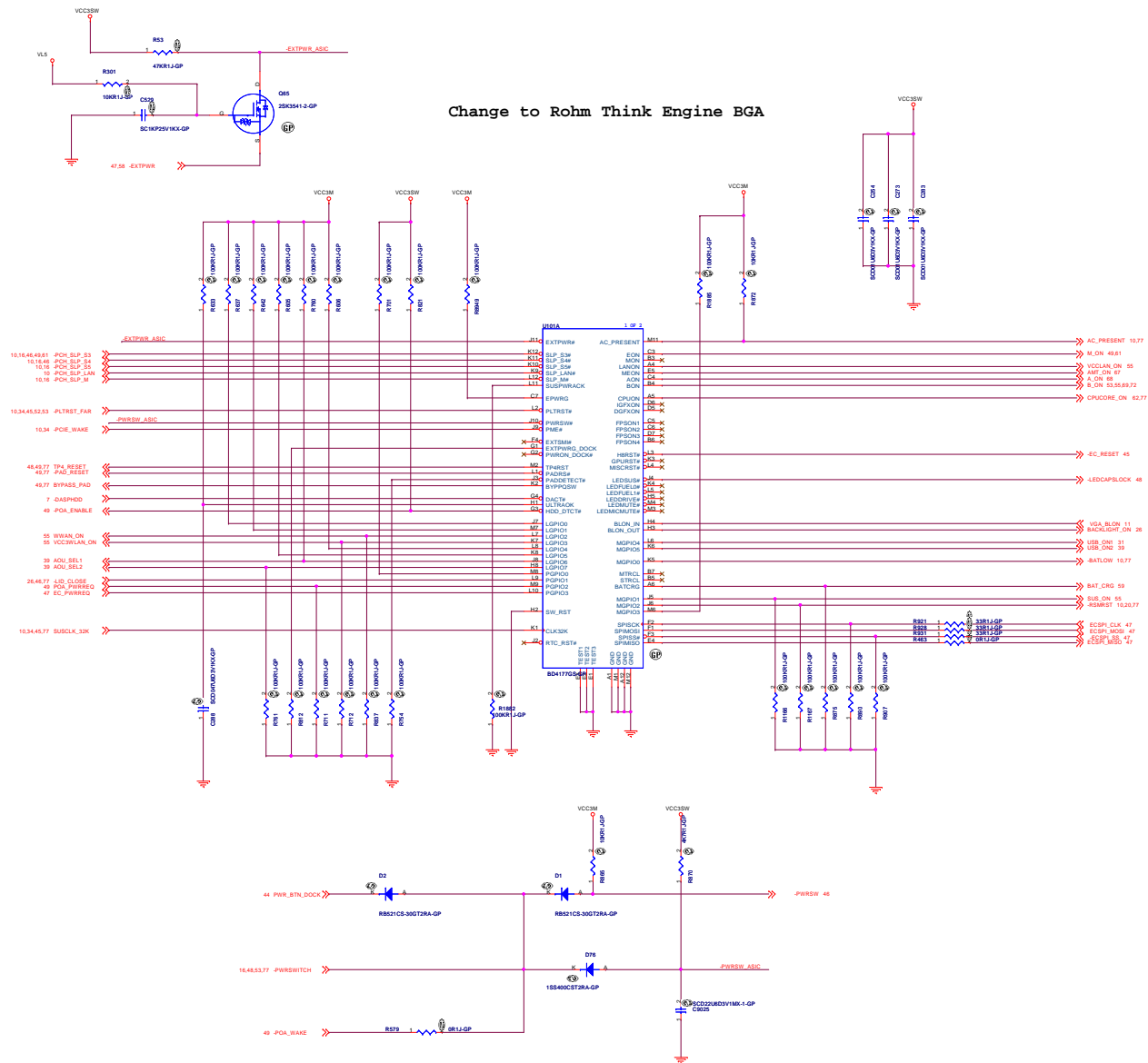
Lenovo Debug Tool I/F

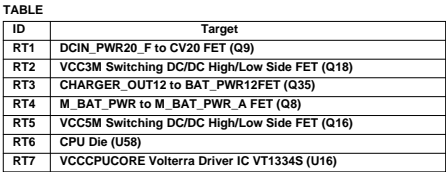


LPC for Debug Card Connector

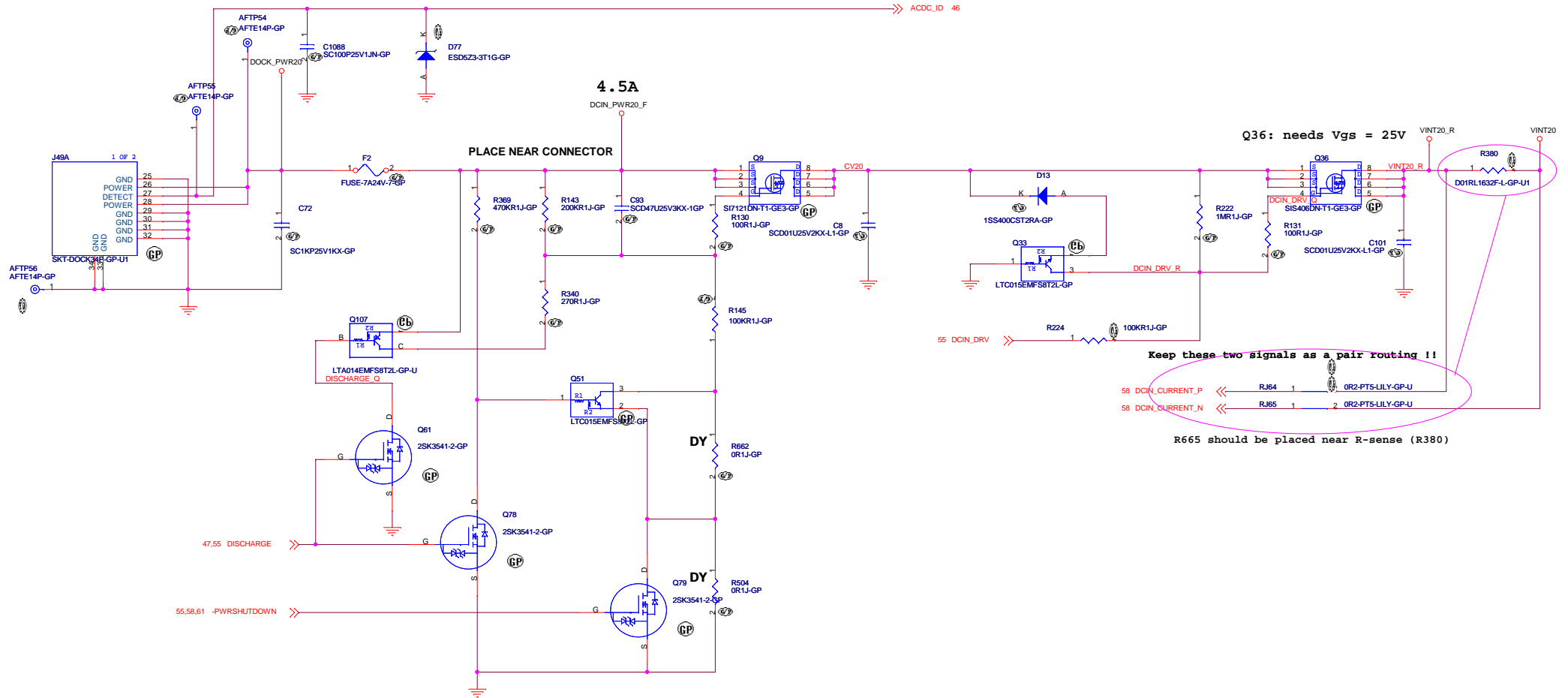


ULT





D77  
ESD5Z3.3T1G/On Semi 83.ESD5Z.0AF  
uClamp3301H/SEMTECH 83.03301.AAF



TABLE

| PEAK SHIFT | YES    | NO     |
|------------|--------|--------|
| R662       | NO-ASM | ASM    |
| R369       | ASM    | NO-ASM |
| Q78        | ASM    | NO-ASM |
| Q51        | ASM    | NO-ASM |

Logic

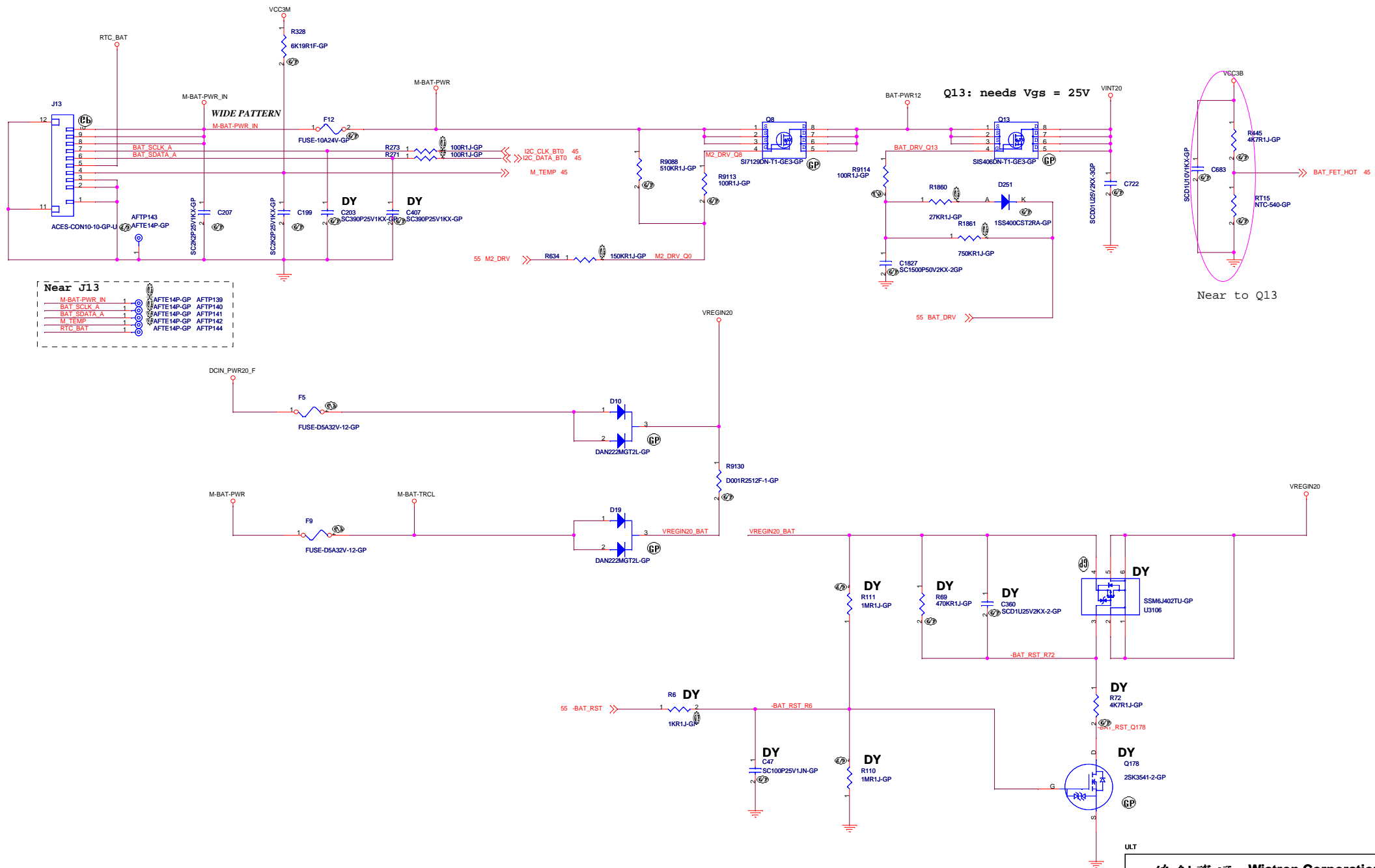
Keep these two signals as a pair routing !!  
58 DCIN\_CURRENT\_P << RJ64 1 OR2-PT5-LILY-GP-U  
58 DCIN\_CURRENT\_N << RJ65 1 OR2-PT5-LILY-GP-U  
R665 should be placed near R-sense (R380)

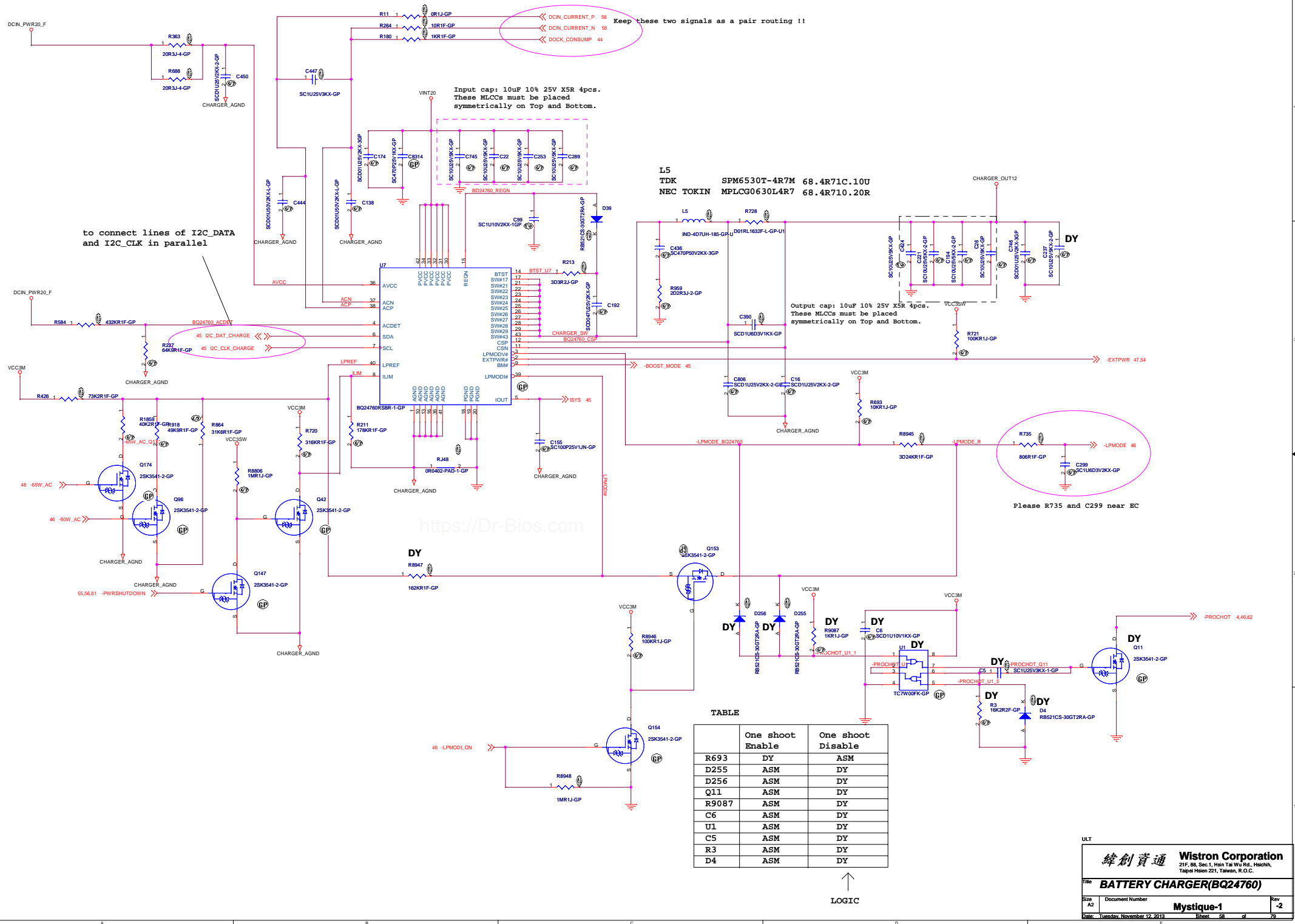
ULT

|                                                                                                                  |                                      |
|------------------------------------------------------------------------------------------------------------------|--------------------------------------|
| <b>緯創資通 Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |                                      |
| Title <b>DC-IN</b>                                                                                               |                                      |
| Size<br>Custom                                                                                                   | Document Number<br><b>Mystique-1</b> |
| Date: Tuesday, November 12, 2013                                                                                 | Sheet 56 of 79                       |

Rev  
-2







Keep these two signals as a pair routing !!

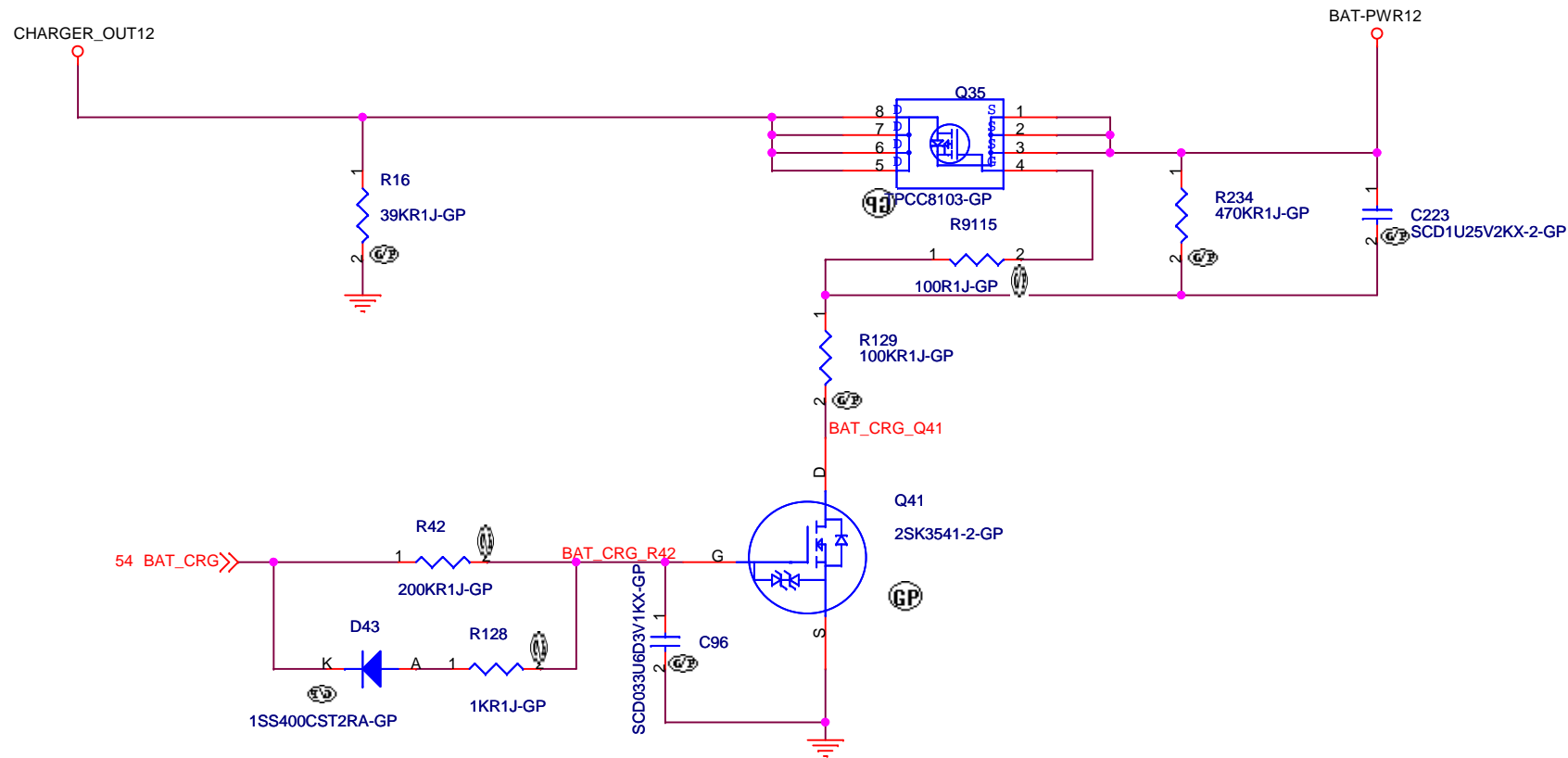
Input cap: 10uF 10% 25V X5R 4pcs.  
These MLCCs must be placed  
symmetrically on Top and Bottom.

L5  
TDK SPM6530T-4R7M 68.4R71C.10U  
NEC TOKIN MPLCG0630L4R7 68.4R710.20R

Output cap: 10uF 10% 25V X5R 4pcs.  
These MLCCs must be placed  
symmetrically on Top and Bottom.

Please R735 and C299 near EC

| TABLE |                  |                   |
|-------|------------------|-------------------|
|       | One shoot Enable | One shoot Disable |
| R693  | DY               | ASM               |
| D255  | ASM              | DY                |
| D256  | ASM              | DY                |
| Q11   | ASM              | DY                |
| R9087 | ASM              | DY                |
| C6    | ASM              | DY                |
| U1    | ASM              | DY                |
| C5    | ASM              | DY                |
| R3    | ASM              | DY                |
| D4    | ASM              | DY                |



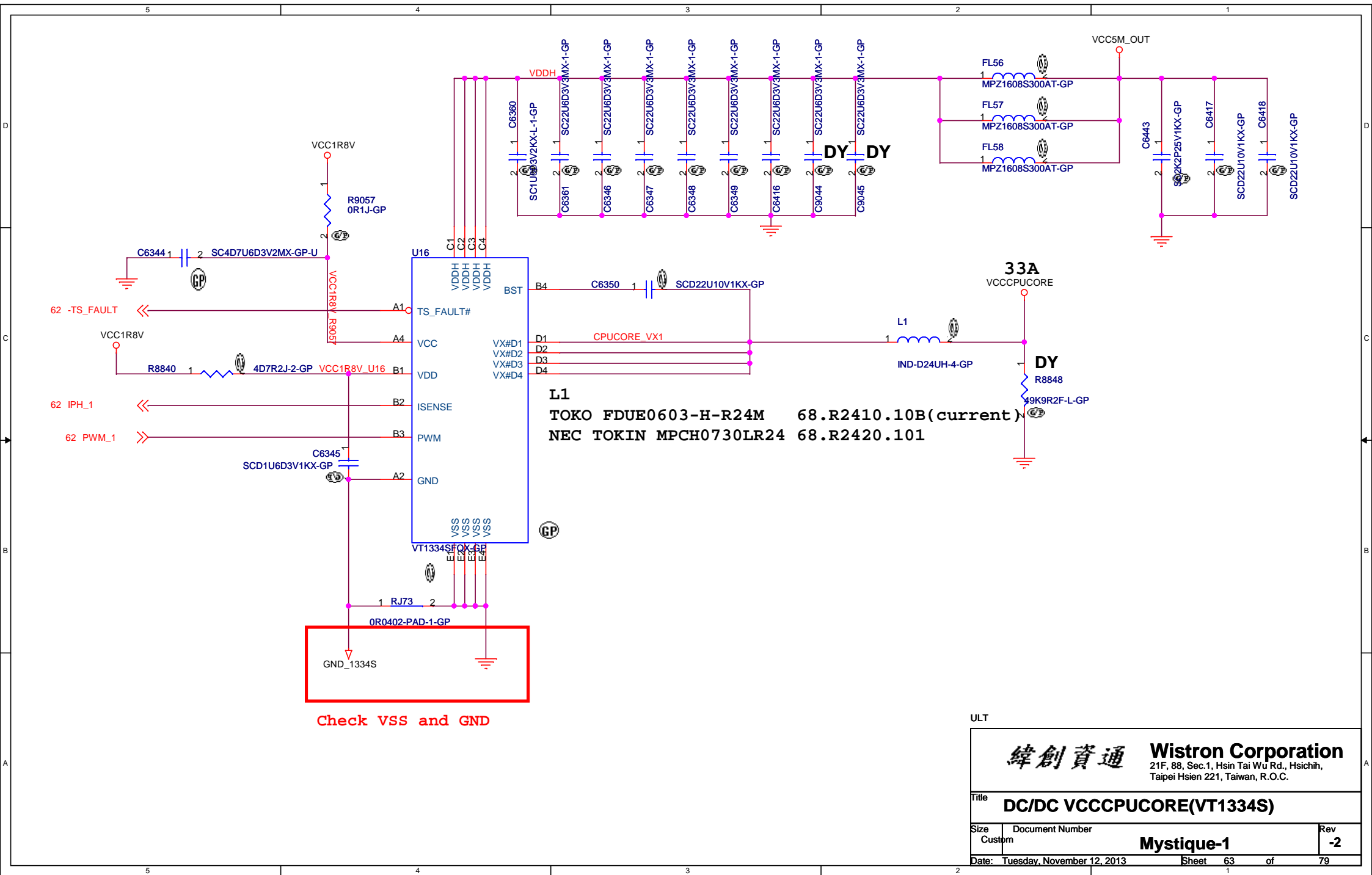
ULT

|                                                                                                                                          |                                                  |
|------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------|
| <div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br/>Taipei Hsien 221, Taiwan, R.O.C.</div> |                                                  |
| Title<br><div>CHARGER SELECTOR</div>                                                                                                     |                                                  |
| Size<br>A4                                                                                                                               | <div>Document Number</div> <div>Mystique-1</div> |
| Date: Tuesday, November 12, 2013                                                                                                         | Sheet 59 of 79                                   |
| Rev<br>-2                                                                                                                                |                                                  |









ULT

緯創資通

**Wistron Corporation**  
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Title **DC/DC VCCCPUCORE(VT1334S)**

Size Document Number  
Custom

**Mystique-1**

Rev  
**-2**

Date: Tuesday, November 12, 2013

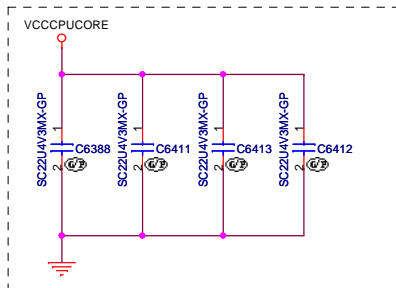
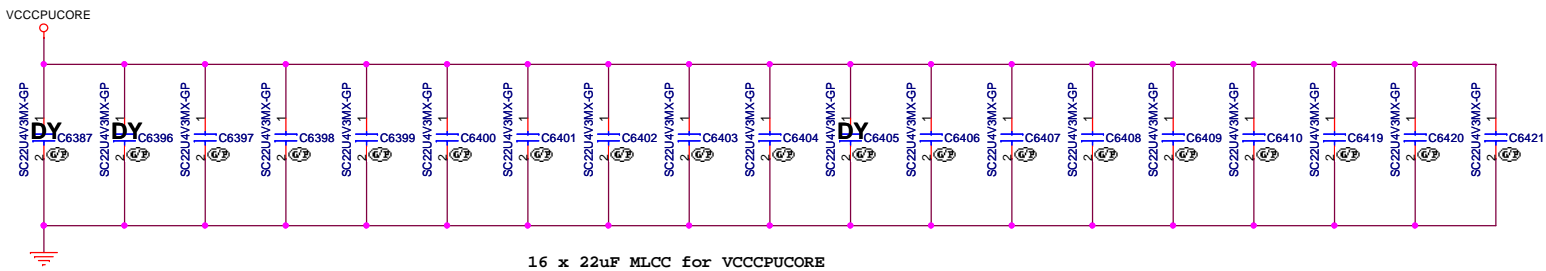
Sheet 63 of 79

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|---------------------------------------------------------------------------------------|--------------------------------------|-------------------------------------------------------------------------------------------------------------|------------------|
|  |                                      | <b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |                  |
| Title <b>BLANK</b>                                                                    |                                      |                                                                                                             |                  |
| Size<br>A4                                                                            | Document Number<br><b>Mystique-1</b> |                                                                                                             | Rev<br><b>-2</b> |
| Date: Tuesday, November 12, 2013                                                      |                                      | Sheet 64                                                                                                    | of 79            |





Lenovo: If change from 0805 to 0603 size, the number of capacitors must increased 20% minimum.  
these four caps are the 25% additional.

ULT

|                                                                                                                         |                                   |
|-------------------------------------------------------------------------------------------------------------------------|-----------------------------------|
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| Title <b>VCCCPUCORE DECOUPLING</b>                                                                                      |                                   |
| Size A3                                                                                                                 | Document Number <b>Mystique-1</b> |
| Date: Tuesday, November 12, 2013                                                                                        | Sheet 65 of 79                    |

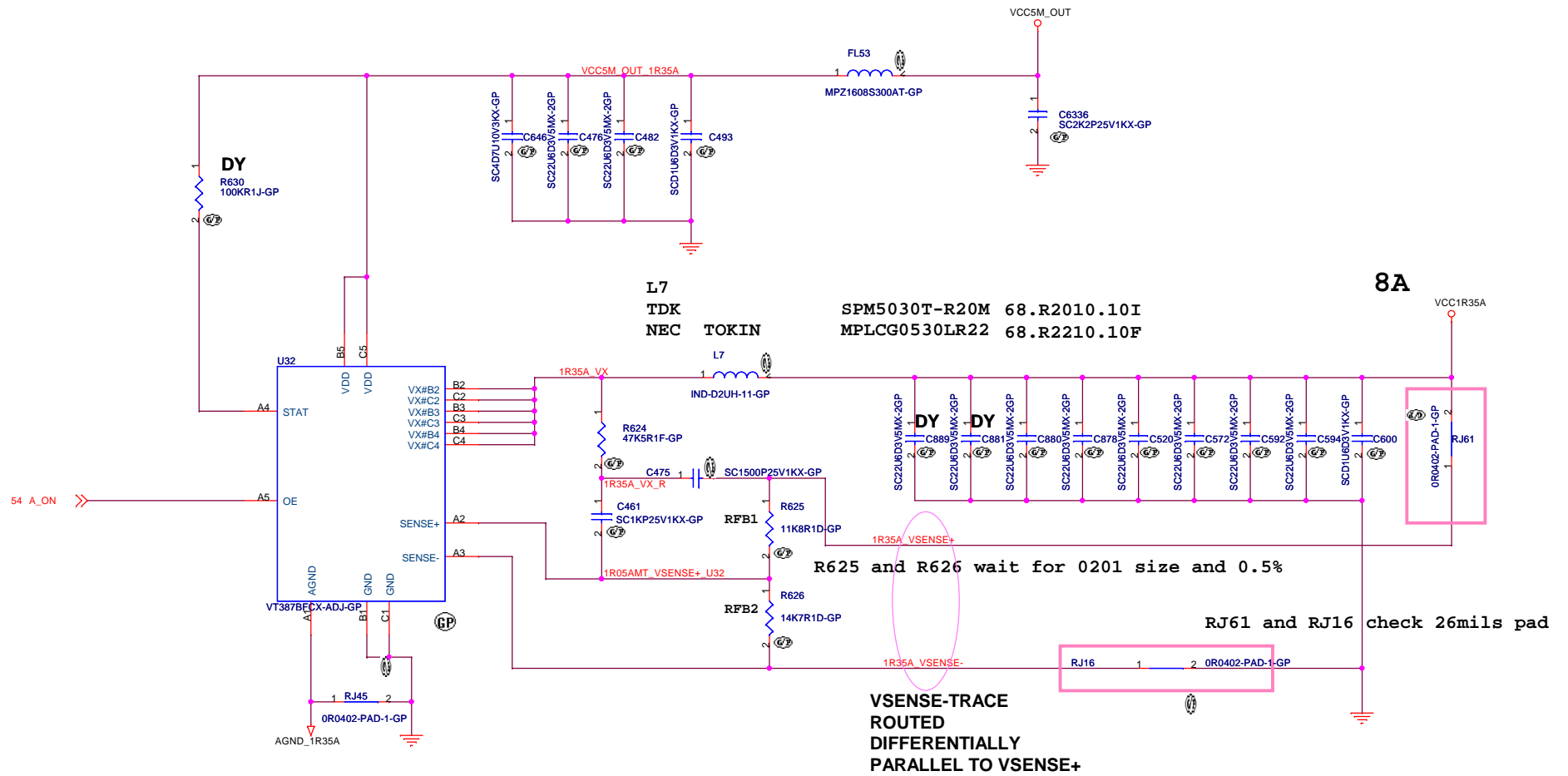
Rev -2

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|---------------------------------------------------------------------------------------|--------------------------------------|-------------------------------------------------------------------------------------------------------------|------------------|
|  |                                      | <b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |                  |
| Title <b>BLANK</b>                                                                    |                                      |                                                                                                             |                  |
| Size<br>A4                                                                            | Document Number<br><b>Mystique-1</b> |                                                                                                             | Rev<br><b>-2</b> |
| Date: Tuesday, November 12, 2013                                                      |                                      | Sheet 66                                                                                                    | of 79            |





TABLE

|                                               |                  |
|-----------------------------------------------|------------------|
| $V_{OUT} = 0.75 \times (1 + R_{FB1}/R_{FB2})$ |                  |
| RFB1                                          | RFB2             |
| R625                                          | R626             |
| 11.8K                                         | 14.7K for 1.35V  |
| 13.3K                                         | 13.3K for 1.50V  |
| 12.1K                                         | 14.7K for 1.367V |
| 13.3K                                         | 13.0K for 1.517V |

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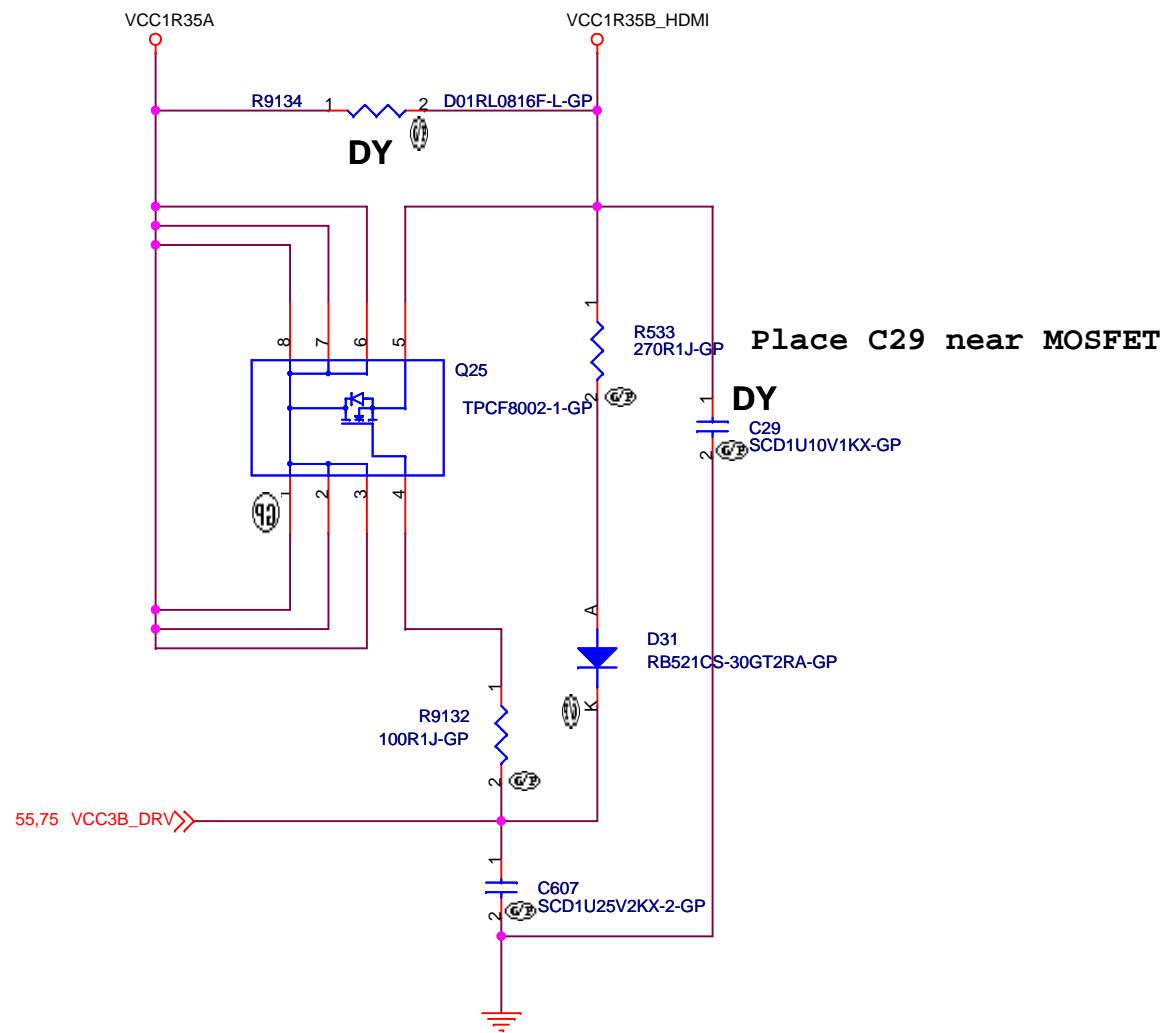


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|-------|----------------------------|----------------------------------------------------------------------------|----------------|
| 緯創資通  |                            | Wistron Corporation                                                        |                |
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| Title |                            |                                                                            |                |
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| Size  | Document Number            |                                                                            | Rev            |
| A4    | Mystique-1                 |                                                                            | -2             |
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500mA



ULT

|                 |                            |                                                                               |          |
|-----------------|----------------------------|-------------------------------------------------------------------------------|----------|
| <div>緯創資通</div> |                            | <div>Wistron Corporation</div>                                                |          |
|                 |                            | 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |          |
| Title           |                            |                                                                               |          |
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| Size            | Document Number            |                                                                               | Rev      |
| A4              | Mystique-1                 |                                                                               | -2       |
| Date:           | Tuesday, November 12, 2013 | Sheet                                                                         | 71 of 79 |

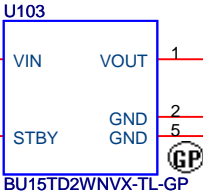
53,54,55,69 B\_ON



VCC3M



C1837  
SCD47U6D3V2KX-L-GP



BU15TD2WNVX-TL-GP



10mA

VCC1R5B



C1838  
SCD47U6D3V2KX-L-GP



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緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title

DC/DC VCC1R5B(BU15TD2WNVX)

Size

A4

Document Number

Mystique-1

Rev

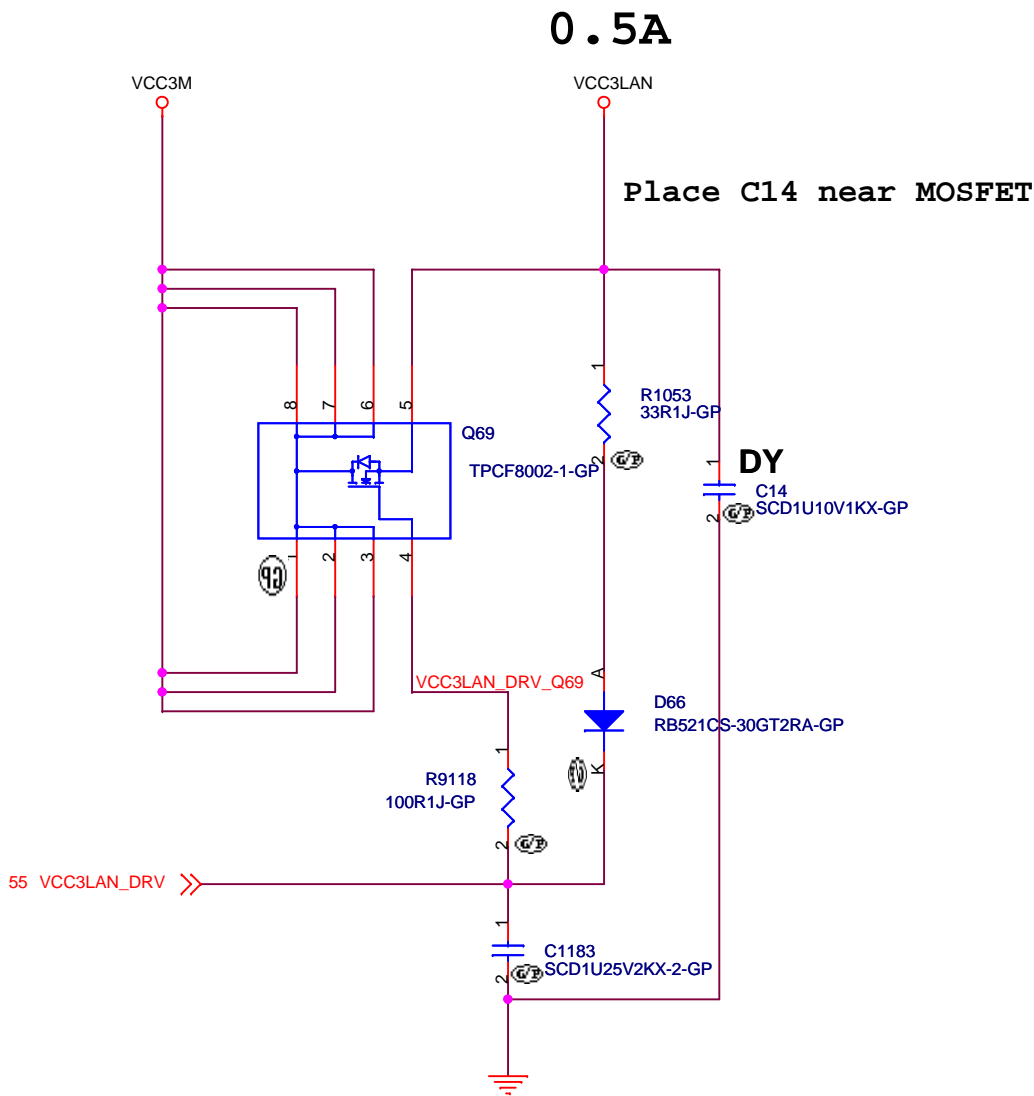
-2

Date: Tuesday, November 12, 2013

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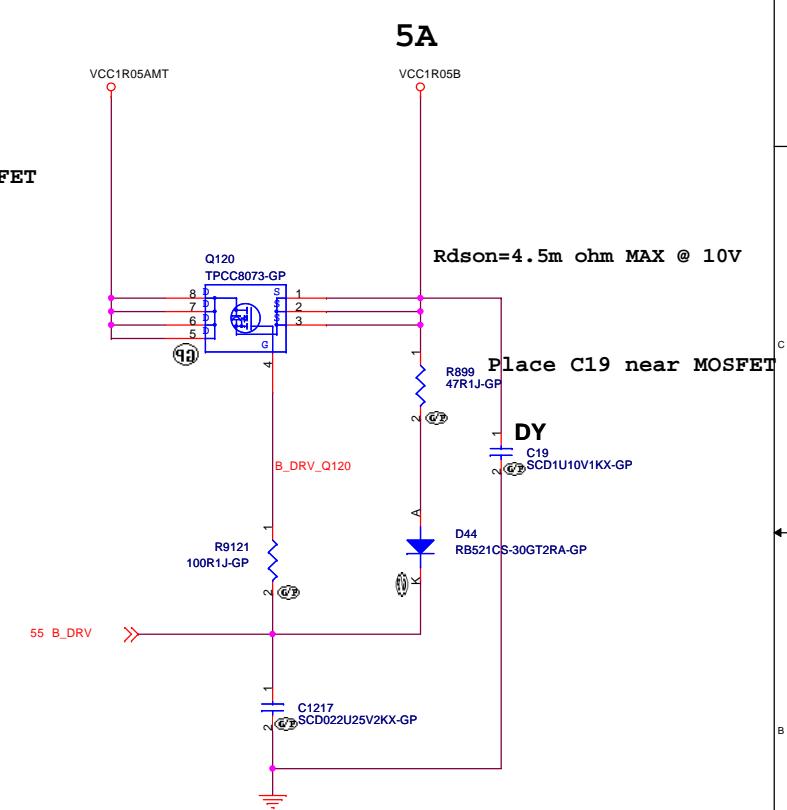
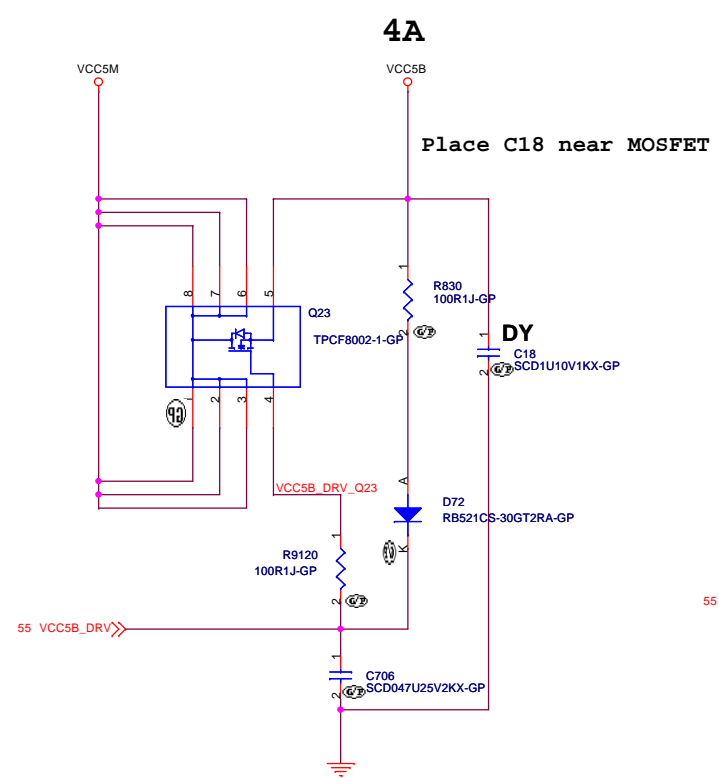
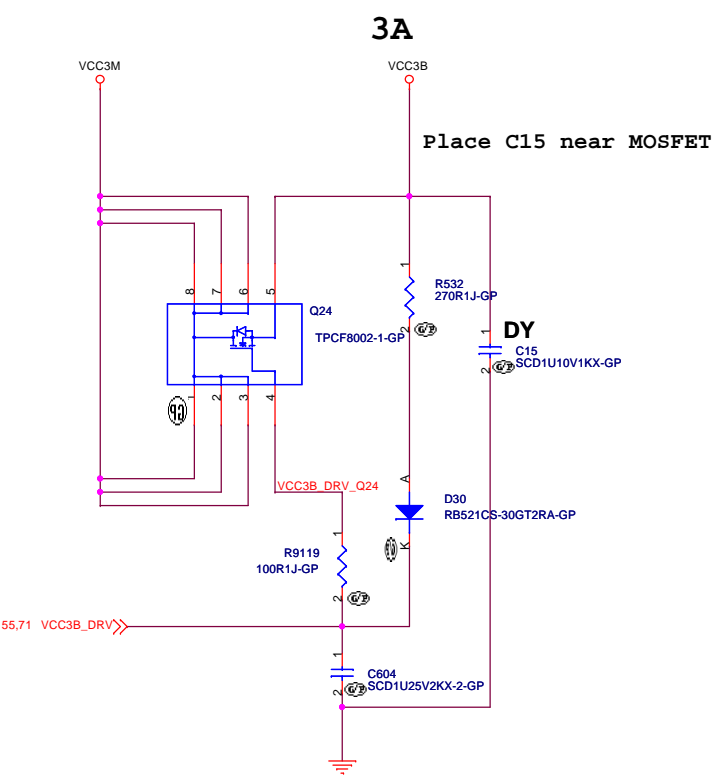


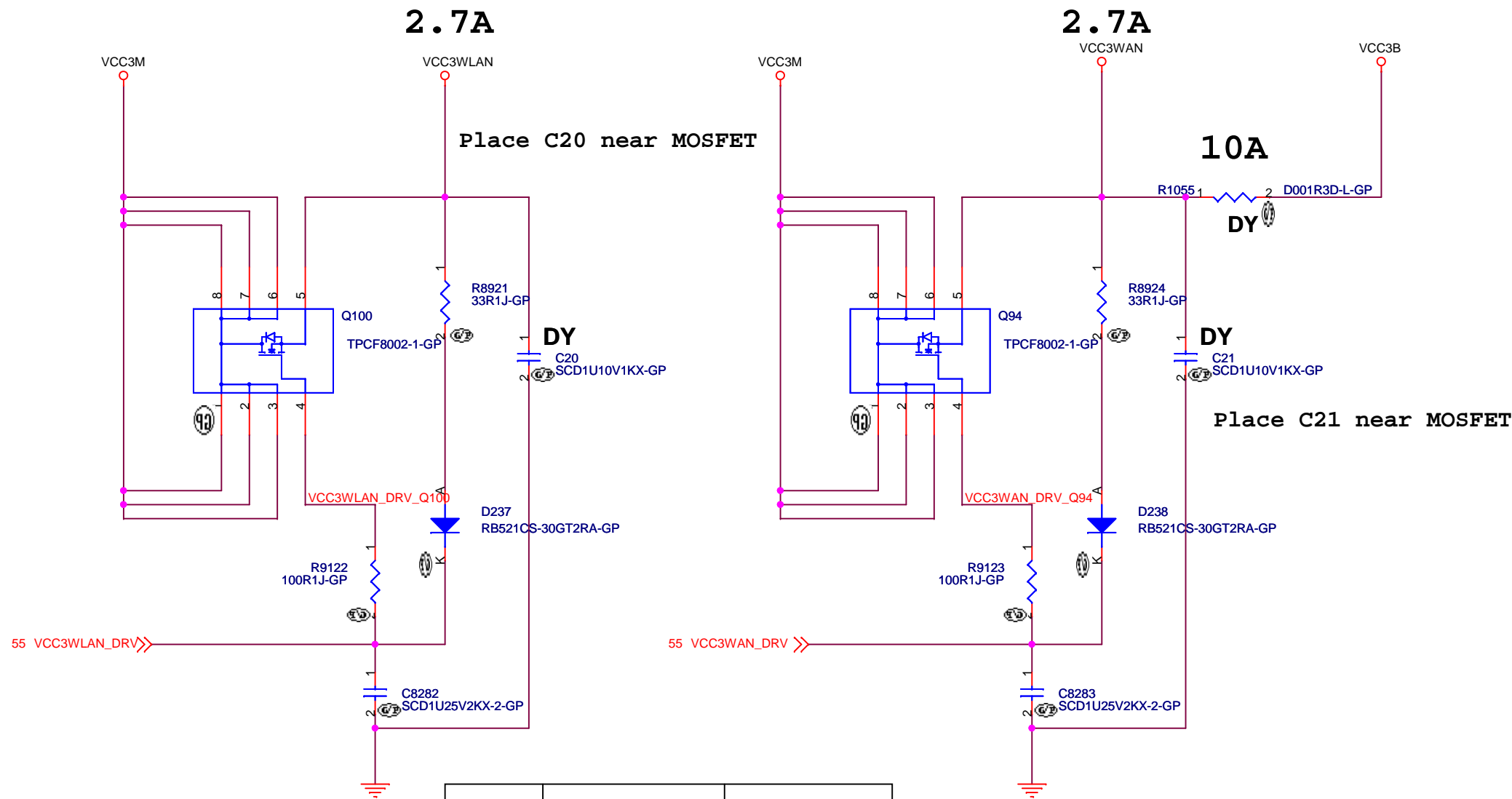




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|                        |                            |                                                                               |          |
|------------------------|----------------------------|-------------------------------------------------------------------------------|----------|
| <div>緯創資通</div>        |                            | <div>Wistron Corporation</div>                                                |          |
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| Title                  |                            |                                                                               |          |
| <div>LOAD SW LAN</div> |                            |                                                                               |          |
| Size                   | Document Number            |                                                                               | Rev      |
| A4                     | <div>Mystique-1</div>      |                                                                               | -2       |
| Date:                  | Tuesday, November 12, 2013 | Sheet                                                                         | 74 of 79 |





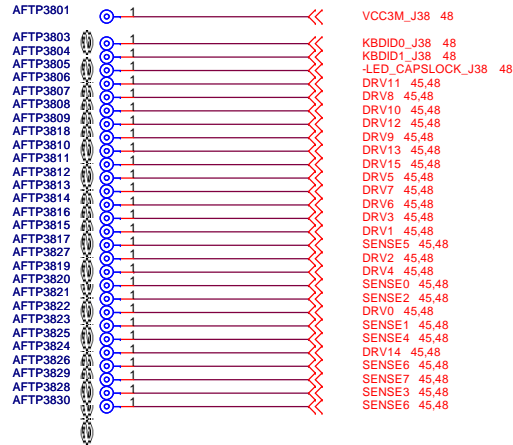
|       |        |        |
|-------|--------|--------|
| AOAC  | YES    | NO     |
| R1055 | NO-ASM | ASM    |
| Q94   | ASM    | NO-ASM |
| R8924 | ASM    | NO-ASM |
| R8926 | ASM    | NO-ASM |
| C8283 | ASM    | NO-ASM |
| D238  | ASM    | NO-ASM |

LOGIC

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|                                                                                                                                                                                                   |                                                 |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------|
|  <b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |                                                 |
| <b>Title</b> <i>LOAD SW WWAN &amp; WLAN</i>                                                                                                                                                       |                                                 |
| <b>Size</b><br>A4                                                                                                                                                                                 | <b>Document Number</b><br><br><b>Mystique-1</b> |
| <b>Date:</b> Tuesday, November 12, 2013                                                                                                                                                           | <b>Rev</b><br>-2                                |
| Sheet 76 of 79                                                                                                                                                                                    |                                                 |

## Near J38(Keyboard connector)



## Near J4 (Speaker)



## Near J25(SIM)



MFG request

## Near J37(Track point connector)



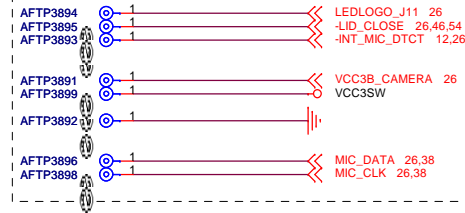
## Near J15(FAN Connector)



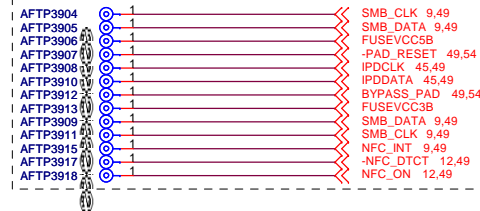
## Near J12(RTC)



## Near J11(LED)



## Near J21 (Clickpad + NFC)



## Near S3



## For Power Switch

### Top side



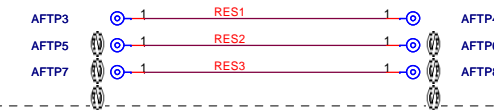
### Bottom side



## For J48 (DFR)



## For PCB resistance measurement when temperature is vary.



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Taipei Hsien 221, Taiwan, R.O.C.

Title TEST POINT

Size A3 Document Number Mystique-1 Rev -2  
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